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# **Adafruitminiesptool Library Documentation**

*Release 1.0*

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ROM loader for ESP chips, works with ESP8266 or ESP32. This is a 'no-stub' loader, so you can't read MD5 or firmware back on ESP8266.

See this document for protocol we're implementing: <https://github.com/espressif/esptool/wiki/Serial-Protocol>

See this for the 'original' code we're miniaturizing: <https://github.com/espressif/esptool/blob/master/esptool.py>

There's a very basic Arduino ROM loader here for ESP32: <https://github.com/arduino-libraries/WiFiNINA/tree/master/examples/Tools/FirmwareUpdater>



This driver depends on:

- [Adafruit CircuitPython](#)

Please ensure all dependencies are available on the CircuitPython filesystem. This is easily achieved by downloading the [Adafruit library and driver bundle](#).

### 1.1 Installing from PyPI

On supported GNU/Linux systems like the Raspberry Pi, you can install the driver locally [from PyPI](#). To install for current user:

```
pip3 install adafruit-circuitpython-miniesptool
```

To install system-wide (this may be required in some cases):

```
sudo pip3 install adafruit-circuitpython-miniesptool
```

To install in a virtual environment in your current project:

```
mkdir project-name && cd project-name
python3 -m venv .env
source .env/bin/activate
pip3 install adafruit-circuitpython-miniesptool
```





## CHAPTER 2

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### Usage Example

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Check the examples folder for demo sketches to upload firmware to ESP8266 and ESP32



## CHAPTER 3

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### Contributing

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Contributions are welcome! Please read our [Code of Conduct](#) before contributing to help this project stay welcoming.



## CHAPTER 4

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### Documentation

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For information on building library documentation, please check out [this guide](#).



## 5.1 Simple test

Ensure your device works with this simple test.

Listing 1: examples/miniesptool\_esp8266program.py

```
1 import time
2 import board
3 import busio
4 from digitalio import DigitalInOut
5 import adafruit_miniesptool
6
7 print("ESP8266 mini prog")
8
9 uart = busio.UART(board.TX, board.RX, baudrate=115200, timeout=1)
10 resetpin = DigitalInOut(board.D5)
11 gpio0pin = DigitalInOut(board.D6)
12 # On ESP8266 we will 'sync' to the baudrate in initialization
13 esptool = adafruit_miniesptool.miniesptool(
14     uart, gpio0pin, resetpin, flashsize=1024 * 1024, baudrate=256000
15 )
16
17 esptool.debug = False
18 esptool.sync()
19
20 print("Synced")
21 print(esptool.chip_name)
22 print("MAC ADDR: ", [hex(i) for i in esptool.mac_addr])
23 esptool.flash_file("esp8266/AT_firmware_1.6.2.0.bin", 0x0)
24 # 0x3FC000 esp_init_data_default_v05.bin
25 esptool.flash_file("esp8266/esp_init_data_default_v05.bin", 0x3FC000)
26 # 0x3FE000 blank.bin
27 esptool.flash_file("esp8266/blank.bin", 0x3FE000)
```

(continues on next page)

```

28 esptool.reset()
29 time.sleep(0.5)

```

## 5.2 adafruit\_miniesptool

ROM loader for ESP chips, works with ESP8266 or ESP32. This is a ‘no-stub’ loader, so you can’t read MD5 or firmware back on ESP8266.

See this document for protocol we’re implementing: <https://github.com/espressif/esptool/wiki/Serial-Protocol>

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### 5.2.1 Implementation Notes

#### Hardware:

#### Software and Dependencies:

- Adafruit CircuitPython firmware for the supported boards: <https://github.com/adafruit/circuitpython/releases>

```
class adafruit_miniesptool.miniesptool (uart, gpio0_pin, reset_pin, *, flashsize, baudrate=115200)
```

A miniature version of esptool, a programming command line tool for ESP8266 and ESP32 chips. This version is minimized to work on CircuitPython boards, so you can burn ESP firmware direct from the CPy disk drive. Handy when you have an ESP module wired to a board and need to upload new AT firmware. Its slow! Expect a few minutes when programming 1 MB flash.

#### **baudrate**

The baudrate of the UART connection. On ESP8266 we cannot change this once we’ve started syncing. On ESP32 we must start at 115200 and then manually change to higher speeds if desired

```
check_command (opcode, buffer, checksum=0, timeout=0.1)
```

Send a command packet, check that the command succeeded and return a tuple with the value and data. See the ESP Serial Protocol for more details on what value/data are

```
static checksum (data, state=239)
```

Calculate checksum of a blob, as it is defined by the ROM

#### **chip\_name**

The specific name of the chip, e.g. ESP8266EX, to the best of our ability to determine without a stub bootloader.

#### **chip\_type**

ESP32 or ESP8266 based on which chip type we’re talking to

#### **debug**

Print out all sent/received UART data plus some debugging output

```
flash_begin (*, size=0, offset=0)
```

Prepare for flashing by attaching SPI chip and erasing the number of blocks required.

```
flash_block (data, seq, timeout=0.1)
```

Send one block of data to program into SPI Flash memory



**flash\_file** (*filename, offset=0, md5=None*)

Program a full, uncompressed binary file into SPI Flash at a given offset. If an ESP32 and md5 string is passed in, will also verify memory. ESP8266 does not have checksum memory verification in ROM

**get\_erase\_size** (*offset, size*)

Calculate an erase size given a specific size in bytes. Provides a workaround for the bootloader erase bug on ESP8266.

**get\_response** (*opcode, timeout=0.1*)

Read response data and decodes the slip packet, then parses out the value/data and returns as a tuple of (value, data) where each is a list of bytes

**mac\_addr**

The MAC address burned into the OTP memory of the ESP chip

**md5** (*offset, size*)

On ESP32 we can ask the ROM bootloader to calculate an MD5 on the SPI flash memory, from a location over a size in bytes. Returns a string with the MD5 in lowercase

**read\_register** (*reg*)

Read a register within the ESP chip RAM, returns a 4-element list

**reset** (*program\_mode=False*)

Perform a hard-reset into ROM bootloader using gpio0 and reset

**send\_command** (*opcode, buffer*)

Send a slip-encoded, checksummed command over the UART, does not check response

**static slip\_encode** (*buffer*)

Take a bytearray buffer and return back a new bytearray where 0xdb is replaced with 0xdb 0xdd and 0xc0 is replaced with 0xdb 0xdc

**sync** ()

Put into ROM bootload mode & attempt to synchronize with the ESP ROM bootloader, we will retry a few times



## CHAPTER 6

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### Indices and tables

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