
libpci Documentation

Release (0, 2, 0, 'dev', 0)

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April 24, 2015

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Contents:

Pure Python, high-level bindings to libpci

1.1 Features

- Free software: LGPLv3 license
- Documentation: <https://libpci.readthedocs.org>.
- Use high-level, pythonic APIs to work with libpci

Installation

At the command line:

```
$ easy_install libpci
```

Or, if you have virtualenvwrapper installed:

```
$ mkvirtualenv libpci  
$ pip install libpci
```

Usage

3.1 Reference

Pure-python, high-level bindings to libpci.

class `libpci.LibPCI`

Pythonic wrapper for libpci.

This class exposes access to libpci functions in a way that is more friendly to work with from Python.

Note: Not all APIs are supported yet.

__del__()

Release wrapper resources.

__enter__()

Enter a context manager.

Returns `self`

Raises ValueError If `closed()` is True

__exit__(*args)

Exit a context manager.

This method calls `close()`.

__init__()

Initialize the wrapper.

__weakref__

list of weak references to the object (if defined)

close()

Release libpci resources.

closed

Flag determining if libpci resources have been released.

flag_cache

Cache names retrieved from the network.

flag_mixed

Use both names and numbers.

flag_network

Allow network access during lookup.

flag_no_numbers

Don't generate numeric names.

flag_numeric

Generate numeric names.

flag_refresh_cache

Refresh cache during the next lookup.

flag_skip_local

Skip local database when performing lookups.

lookup_device_name (*vendor_id*, *device_id*)

Lookup the name of a given device.

Parameters

- **vendor_id** – PCI vendor identifier
- **device_id** – PCI device identifier

Ptype vendor_id int

Ptype device_id int

Returns Name of the PCI device.

Note: Lookup respects various flag properties that impact the behavior in case the name cannot be found in the local database. Refer to the documentation of each of the `flag_` properties.

lookup_subsystem_device_name (*vendor_id*, *device_id*, *subvendor_id*, *subdevice_id*)

Lookup the name of a given subsystem device.

Parameters

- **vendor_id** – PCI vendor identifier
- **device_id** – PCI device identifier
- **subvendor_id** – PCI subvendor identifier
- **device_id** – PCI subdevice identifier

Ptype vendor_id int

Ptype device_id int

Ptype subvendor_id int

Ptype subdevice_id int

Returns Name of the PCI subsystem device.

Note: Lookup respects various flag properties that impact the behavior in case the name cannot be found in the local database. Refer to the documentation of each of the `flag_` properties.

lookup_vendor_name (*vendor_id*)

Lookup the name of a given vendor.

Parameters **vendor_id** – PCI vendor identifier

Ptype vendor_id int

Returns Name of the PCI vendor.

Note: Lookup respects various flag properties that impact the behavior in case the name cannot be found in the local database. Refer to the documentation of each of the `flag_` properties.

3.2 LibPCI Internals

3.2.1 Macros

Macros found in `pci.h`.

```
libpci._macros.PCI_CAP_ID_PM = <PCI_CAP_ID_PM: 1>
    Constant from pci.h

libpci._macros.PCI_HT_EH_CRCFE = <PCI_HT_EH_CRCFE: 64>
    Constant from pci.h

libpci._macros.PCI_EXP_DEV2_ARI = <PCI_EXP_DEV2_ARI: 32>
    Constant from pci.h

libpci._macros.PCI_HT_VCS_NFCINT = <PCI_HT_VCS_NFCINT: 13>
    Constant from pci.h

libpci._macros.PCI_HT_PRI_LCNF1 = <PCI_HT_PRI_LCNF1: 10>
    Constant from pci.h

libpci._macros.PCI_HT_SW_CMD_VIBNFT = <PCI_HT_SW_CMD_VIBNFT: 1024>
    Constant from pci.h

libpci._macros.PCI_ERR_COR_RCVR = <PCI_ERR_COR_RCVR: 1>
    Constant from pci.h

libpci._macros.PCI_SID_ESR_NSLOTS = <PCI_SID_ESR_NSLOTS: 31>
    Constant from pci.h

libpci._macros.PCI_EXP_DEVCTL_NOSNOOP = <PCI_EXP_DEVCTL_NOSNOOP: 2048>
    Constant from pci.h

libpci._macros.PCI_CLASS_INPUT_KEYBOARD = <PCI_CLASS_INPUT_KEYBOARD: 2304>
    Constant from pci.h

libpci._macros.PCI_EXP_TYPE_UPSTREAM = <PCI_EXP_TYPE_UPSTREAM: 5>
    Constant from pci.h

libpci._macros.PCI_CLASS_DOCKING_OTHER = <PCI_CLASS_DOCKING_OTHER: 2688>
    Constant from pci.h

libpci._macros.PCI_HT_EH_SERRNFE = <PCI_HT_EH_SERRNFE: 32768>
    Constant from pci.h

libpci._macros.PCI_CLASS_DISPLAY_XGA = <PCI_CLASS_DISPLAY_XGA: 769>
    Constant from pci.h

libpci._macros.PCI_CLASS_PROCESSOR_486 = <PCI_CLASS_PROCESSOR_486: 2817>
    Constant from pci.h

libpci._macros.PCI_EXT_CAP_ID_DSN = <PCI_EXT_CAP_ID_DSN: 3>
    Constant from pci.h
```

`libpci._macros.PCI_HT_SW_SWINF = <PCI_HT_SW_SWINF: 8>`
Constant from pci.h

`libpci._macros.PCI_SUBSYSTEM_ID = <PCI_SUBSYSTEM_ID: 46>`
Constant from pci.h

`libpci._macros.PCI_CLASS_SERIAL_SMBUS = <PCI_CLASS_SERIAL_SMBUS: 3077>`
Constant from pci.h

`libpci._macros.PCI_HT_LFRER_800 = <PCI_HT_LFRER_800: 5>`
Constant from pci.h

`libpci._macros.PCI_HT_IDC_IDR_POL = <PCI_HT_IDC_IDR_POL: 268435458>`
Constant from pci.h

`libpci._macros.PCI_HT_SW_S0_HI = <PCI_HT_SW_S0_HI: 1>`
Constant from pci.h

`libpci._macros.PCI_HT_LCNF_LW_2B = <PCI_HT_LCNF_LW_2B: 4>`
Constant from pci.h

`libpci._macros.PCI_STATUS_DEVSEL_SLOW = <PCI_STATUS_DEVSEL_SLOW: 1024>`
Constant from pci.h

`libpci._macros.PCI_DEVICE_ID = <PCI_DEVICE_ID: 2>`
Constant from pci.h

`libpci._macros.PCI_CLASS_NOT_DEFINED = <PCI_CLASS_NOT_DEFINED: 0>`
Constant from pci.h

`libpci._macros.PCI_ACS_CTRL_FORWARD = <PCI_ACS_CTRL_FORWARD: 16>`
Constant from pci.h

`libpci._macros.PCI_AGP_STATUS_ITA_COH = <PCI_AGP_STATUS_ITA_COH: 256>`
Constant from pci.h

`libpci._macros.PCI_EXP_LNKCAP_CLOCKPM = <PCI_EXP_LNKCAP_CLOCKPM: 262144>`
Constant from pci.h

`libpci._macros.PCI_IO_RANGE_TYPE_32 = <PCI_IO_RANGE_TYPE_32: 1>`
Constant from pci.h

`libpci._macros.PCI_CLASS_MULTIMEDIA_VIDEO = <PCI_CLASS_MULTIMEDIA_VIDEO: 1024>`
Constant from pci.h

`libpci._macros.PCI_CLASS_CRYPT_OTHER = <PCI_CLASS_CRYPT_OTHER: 4224>`
Constant from pci.h

`libpci._macros.PCI_CLASS_SYSTEM_DMA = <PCI_CLASS_SYSTEM_DMA: 2049>`
Constant from pci.h

`libpci._macros.PCI_CLASS_MEMORY_RAM = <PCI_CLASS_MEMORY_RAM: 1280>`
Constant from pci.h

`libpci._macros.PCI_CLASS_SERIAL_FIBER = <PCI_CLASS_SERIAL_FIBER: 3076>`
Constant from pci.h

`libpci._macros.PCI_SATA_HBA_BARS = <PCI_SATA_HBA_BARS: 4>`
Constant from pci.h

`libpci._macros.PCI_HT_SEC_FTR_UCNFE = <PCI_HT_SEC_FTR_UCNFE: 512>`
Constant from pci.h

```

libpci._macros.PCI_BASE_ADDRESS_MEM_TYPE_64 = <PCI_BASE_ADDRESS_MEM_TYPE_64: 4>
    Constant from pci.h

libpci._macros.PCI_HT_EH_OFE = <PCI_HT_EH_OFE: 8>
    Constant from pci.h

libpci._macros.PCI_EXP_LNKCTL_BWMIE = <PCI_EXP_LNKCTL_BWMIE: 1024>
    Constant from pci.h

libpci._macros.PCI_HT_LFRER_400 = <PCI_HT_LFRER_400: 2>
    Constant from pci.h

libpci._macros.PCI_AGP_COMMAND_RATE1 = <PCI_AGP_COMMAND_RATE1: 1>
    Constant from pci.h

libpci._macros.PCI_HT_LCTR_CST = <PCI_HT_LCTR_CST: 4>
    Constant from pci.h

libpci._macros.PCI_EXT_CAP_ID_VNDR = <PCI_EXT_CAP_ID_VNDR: 11>
    Constant from pci.h

libpci._macros.PCI_PCIX_COMMAND_RESERVED = <PCI_PCIX_COMMAND_RESERVED: 3968>
    Constant from pci.h

libpci._macros.PCI_EXP_SLTCAP = <PCI_EXP_SLTCAP: 20>
    Constant from pci.h

libpci._macros.PCI_PCIX_STATUS_533MHZ = <PCI_PCIX_STATUS_533MHZ: 2147483648>
    Constant from pci.h

libpci._macros.PCI_IOV_CTRL_VFMIE = <PCI_IOV_CTRL_VFMIE: 4>
    Constant from pci.h

libpci._macros.PCI_CLASS_STORAGE_ATA = <PCI_CLASS_STORAGE_ATA: 261>
    Constant from pci.h

libpci._macros.PCI_HT_MSIM_CMD_FIXD = <PCI_HT_MSIM_CMD_FIXD: 2>
    Constant from pci.h

libpci._macros.PCI_EXP_DEVSTA2 = <PCI_EXP_DEVSTA2: 42>
    Constant from pci.h

libpci._macros.PCI_HT_RM_STS0 = <PCI_HT_RM_STS0: 5>
    Constant from pci.h

libpci._macros.PCI_CLASS_SATELLITE_AUDIO = <PCI_CLASS_SATELLITE_AUDIO: 3841>
    Constant from pci.h

libpci._macros.PCI_SEC_LATENCY_TIMER = <PCI_SEC_LATENCY_TIMER: 27>
    Constant from pci.h

libpci._macros.PCI_BASE_CLASS_CRYPT = <PCI_BASE_CLASS_CRYPT: 16>
    Constant from pci.h

libpci._macros.PCI_EXP_TYPE_PCI_BRIDGE = <PCI_EXP_TYPE_PCI_BRIDGE: 7>
    Constant from pci.h

libpci._macros.PCI_HT_SEC_SIZEOF = <PCI_HT_SEC_SIZEOF: 24>
    Constant from pci.h

libpci._macros.PCI_IOV_CTRL_VFE = <PCI_IOV_CTRL_VFE: 1>
    Constant from pci.h

```

`libpci._macros.PCI_PCIX_BRIDGE_SEC_STATUS_SC_DISCARDED = <PCI_PCIX_BRIDGE_SEC_STATUS_SC_DISCARDED: 1>`
Constant from pci.h

`libpci._macros.PCI_CLASS_NETWORK_ATM = <PCI_CLASS_NETWORK_ATM: 515>`
Constant from pci.h

`libpci._macros.PCI_BASE_CLASS_SERIAL = <PCI_BASE_CLASS_SERIAL: 12>`
Constant from pci.h

`libpci._macros.PCI_EXP_SLTSTA_MRLS = <PCI_EXP_SLTSTA_MRLS: 4>`
Constant from pci.h

`libpci._macros.PCI_MSI_MASK_BIT_32 = <PCI_MSI_MASK_BIT_32: 12>`
Constant from pci.h

`libpci._macros.PCI_HT_RM_CNT1 = <PCI_HT_RM_CNT1: 10>`
Constant from pci.h

`libpci._macros.PCI_ARI_CAP = <PCI_ARI_CAP: 4>`
Constant from pci.h

`libpci._macros.PCI_STATUS_REC_TARGET_ABORT = <PCI_STATUS_REC_TARGET_ABORT: 4096>`
Constant from pci.h

`libpci._macros.PCI_COMMAND = <PCI_COMMAND: 4>`
Constant from pci.h

`libpci._macros.PCI_CAP_ID_VPD = <PCI_CAP_ID_VPD: 3>`
Constant from pci.h

`libpci._macros.PCI_COMMAND_MASTER = <PCI_COMMAND_MASTER: 4>`
Constant from pci.h

`libpci._macros.PCI_HT_LCTR_LSEN = <PCI_HT_LCTR_LSEN: 8192>`
Constant from pci.h

`libpci._macros.PCI_CLASS_BRIDGE_RACEWAY = <PCI_CLASS_BRIDGE_RACEWAY: 1544>`
Constant from pci.h

`libpci._macros.PCI_HT_SEC_LCTR = <PCI_HT_SEC_LCTR: 4>`
Constant from pci.h

`libpci._macros.PCI_CLASS_SERIAL_ACCESS = <PCI_CLASS_SERIAL_ACCESS: 3073>`
Constant from pci.h

`libpci._macros.PCI_PCIX_STATUS_DESIGNATED_MAX_OUTSTANDING_SPLIT_TRANS = <PCI_PCIX_STATUS_DESIGNATED_MAX_OUTSTANDING_SPLIT_TRANS: 1>`
Constant from pci.h

`libpci._macros.PCI_HT_RID_RID = <PCI_HT_RID_RID: 2>`
Constant from pci.h

`libpci._macros.PCI_EXP_LNKCTL_CLOCKPM = <PCI_EXP_LNKCTL_CLOCKPM: 256>`
Constant from pci.h

`libpci._macros.PCI_PCIX_STATUS_RCVD_SC_ERR_MESS = <PCI_PCIX_STATUS_RCVD_SC_ERR_MESS: 536870912>`
Constant from pci.h

`libpci._macros.PCI_HT_CMD_TYP_X86 = <PCI_HT_CMD_TYP_X86: 51200>`
Constant from pci.h

`libpci._macros.PCI_CAP_ID_AF = <PCI_CAP_ID_AF: 19>`
Constant from pci.h

```
libpci._macros.PCI_PM_CAP_PME_D2 = <PCI_PM_CAP_PME_D2: 8192>
    Constant from pci.h

libpci._macros.PCI_ERR_ROOT_COR_SRC = <PCI_ERR_ROOT_COR_SRC: 52>
    Constant from pci.h

libpci._macros.PCI_ERR_UNC_RX_OVER = <PCI_ERR_UNC_RX_OVER: 131072>
    Constant from pci.h

libpci._macros.PCI_CAP_ID_AGP = <PCI_CAP_ID_AGP: 2>
    Constant from pci.h

libpci._macros.PCI_ERR_UNC_DLP = <PCI_ERR_UNC_DLP: 16>
    Constant from pci.h

libpci._macros.PCI_CAP_ID_DBG = <PCI_CAP_ID_DBG: 10>
    Constant from pci.h

libpci._macros.PCI_ERR_UNC_COMP_ABORT = <PCI_ERR_UNC_COMP_ABORT: 32768>
    Constant from pci.h

libpci._macros.PCI_EXP_DEVSTA_FED = <PCI_EXP_DEVSTA_FED: 4>
    Constant from pci.h

libpci._macros.PCI_CB_PRIMARY_BUS = <PCI_CB_PRIMARY_BUS: 24>
    Constant from pci.h

libpci._macros.PCI_HT_MSIM_ADDR_HI = <PCI_HT_MSIM_ADDR_HI: 8>
    Constant from pci.h

libpci._macros.PCI_BIST_START = <PCI_BIST_START: 64>
    Constant from pci.h

libpci._macros.PCI_COMMAND_MEMORY = <PCI_COMMAND_MEMORY: 2>
    Constant from pci.h

libpci._macros.PCI_BASE_ADDRESS_MEM_TYPE_32 = <PCI_BASE_ADDRESS_MEM_TYPE_32: 0>
    Constant from pci.h

libpci._macros.PCI_EXP_LNKSTA_AUTBW = <PCI_EXP_LNKSTA_AUTBW: 32768>
    Constant from pci.h

libpci._macros.PCI_HT_AM_CMD_IOSIZ = <PCI_HT_AM_CMD_IOSIZ: 496>
    Constant from pci.h

libpci._macros.PCI_AF_CAP_FLR = <PCI_AF_CAP_FLR: 2>
    Constant from pci.h

libpci._macros.PCI_BIST_CODE_MASK = <PCI_BIST_CODE_MASK: 15>
    Constant from pci.h

libpci._macros.PCI_STATUS_INTx = <PCI_STATUS_INTx: 8>
    Constant from pci.h

libpci._macros.PCI_PM_CTRL = <PCI_PM_CTRL: 4>
    Constant from pci.h

libpci._macros.PCI_EXP_SLTCAP_HPC = <PCI_EXP_SLTCAP_HPC: 64>
    Constant from pci.h

libpci._macros.PCI_ATS_CAP = <PCI_ATS_CAP: 4>
    Constant from pci.h
```

libpci._macros.PCI_HT_IDC_DATA = <PCI_HT_IDC_DATA: 4>
Constant from pci.h

libpci._macros.PCI_AGP_STATUS_SBA = <PCI_AGP_STATUS_SBA: 512>
Constant from pci.h

libpci._macros.PCI_PCIX_BRIDGE_STATUS_BUS = <PCI_PCIX_BRIDGE_STATUS_BUS: 65280>
Constant from pci.h

libpci._macros.PCI_CLASS_SERIAL_USB = <PCI_CLASS_SERIAL_USB: 3075>
Constant from pci.h

libpci._macros.PCI_PM_BPCC_ENABLE = <PCI_PM_BPCC_ENABLE: 128>
Constant from pci.h

libpci._macros.PCI_EXP_TYPE_DOWNSTREAM = <PCI_EXP_TYPE_DOWNSTREAM: 6>
Constant from pci.h

libpci._macros.PCI_EXP_SLTSTA_MRL_ST = <PCI_EXP_SLTSTA_MRL_ST: 32>
Constant from pci.h

libpci._macros.PCI_AGP_STATUS_GART64 = <PCI_AGP_STATUS_GART64: 128>
Constant from pci.h

libpci._macros.PCI_HT_RID_SIZEOF = <PCI_HT_RID_SIZEOF: 4>
Constant from pci.h

libpci._macros.PCI_HT_LCNF_DFI = <PCI_HT_LCNF_DFI: 8>
Constant from pci.h

libpci._macros.PCI_HT_SEC_CMD = <PCI_HT_SEC_CMD: 2>
Constant from pci.h

libpci._macros.PCI_HT_LCTR_CFE = <PCI_HT_LCTR_CFE: 8>
Constant from pci.h

libpci._macros.PCI_HT_CMD_TYP_ECDSA = <PCI_HT_CMD_TYP_ECDSA: 38912>
Constant from pci.h

libpci._macros.PCI_HT_RID_MIN = <PCI_HT_RID_MIN: 31>
Constant from pci.h

libpci._macros.PCI_ERR_COR_STATUS = <PCI_ERR_COR_STATUS: 16>
Constant from pci.h

libpci._macros.PCI_CLASS_WIRELESS_CONSUMER_IR = <PCI_CLASS_WIRELESS_CONSUMER_IR: 3329>
Constant from pci.h

libpci._macros.PCI_BIST = <PCI_BIST: 15>
Constant from pci.h

libpci._macros.PCI_COMMAND_SERR = <PCI_COMMAND_SERR: 256>
Constant from pci.h

libpci._macros.PCI_PREF_BASE_UPPER32 = <PCI_PREF_BASE_UPPER32: 40>
Constant from pci.h

libpci._macros.PCI_CLASS_NETWORK_OTHER = <PCI_CLASS_NETWORK_OTHER: 640>
Constant from pci.h

libpci._macros.PCI_HT_SEC_LFCAP = <PCI_HT_SEC_LFCAP: 10>
Constant from pci.h

```
libpci._macros.PCI_SEC_STATUS = <PCI_SEC_STATUS: 30>
    Constant from pci.h

libpci._macros.PCI_HT_CMD_TYP_UIDC = <PCI_HT_CMD_TYP_UIDC: 36864>
    Constant from pci.h

libpci._macros.PCI_HT_LFRER_OV = <PCI_HT_LFRER_OV: 32>
    Constant from pci.h

libpci._macros.PCI_EXT_CAP_ID_PB = <PCI_EXT_CAP_ID_PB: 4>
    Constant from pci.h

libpci._macros.PCI_HT_LFRER_FREQ = <PCI_HT_LFRER_FREQ: 15>
    Constant from pci.h

libpci._macros.PCI_PCIX_BRIDGE_STR_CAPACITY = <PCI_PCIX_BRIDGE_STR_CAPACITY: 65535>
    Constant from pci.h

libpci._macros.PCI_EXP_SLTSTA_LLCHG = <PCI_EXP_SLTSTA_LLCHG: 256>
    Constant from pci.h

libpci._macros.PCI_HT_LCTR_EXTCTL = <PCI_HT_LCTR_EXTCTL: 16384>
    Constant from pci.h

libpci._macros.PCI_EXP_LINKSTA2_EQU_COMP = <PCI_EXP_LINKSTA2_EQU_COMP: 2>
    Constant from pci.h

libpci._macros.PCI_ERR_COR_REP_ANFE = <PCI_ERR_COR_REP_ANFE: 8192>
    Constant from pci.h

libpci._macros.PCI_HT_AM64_DATA_HI = <PCI_HT_AM64_DATA_HI: 12>
    Constant from pci.h

libpci._macros.PCI_PCIX_BRIDGE_STATUS_DEVICE = <PCI_PCIX_BRIDGE_STATUS_DEVICE: 248>
    Constant from pci.h

libpci._macros.PCI_PM_CAP_PME_D3_COLD = <PCI_PM_CAP_PME_D3_COLD: 32768>
    Constant from pci.h

libpci._macros.PCI_MSI_PENDING_32 = <PCI_MSI_PENDING_32: 16>
    Constant from pci.h

libpci._macros.PCI_PRIMARY_BUS = <PCI_PRIMARY_BUS: 24>
    Constant from pci.h

libpci._macros.PCI_EXP_SLTCAP_PSN = <PCI_EXP_SLTCAP_PSN: 4294443008>
    Constant from pci.h

libpci._macros.PCI_BASE_CLASS_NETWORK = <PCI_BASE_CLASS_NETWORK: 2>
    Constant from pci.h

libpci._macros.PCI_CLASS_INPUT_PEN = <PCI_CLASS_INPUT_PEN: 2305>
    Constant from pci.h

libpci._macros.PCI_PM_CAP_PME_D3_HOT = <PCI_PM_CAP_PME_D3_HOT: 16384>
    Constant from pci.h

libpci._macros.PCI_HT_LFCAP_300 = <PCI_HT_LFCAP_300: 2>
    Constant from pci.h

libpci._macros.PCI_COMMAND_SPECIAL = <PCI_COMMAND_SPECIAL: 8>
    Constant from pci.h
```

```
libpci._macros.PCI_SATA_HBA_REG0 = <PCI_SATA_HBA_REG0: 8>
    Constant from pci.h

libpci._macros.PCI_MSIX_ENABLE = <PCI_MSIX_ENABLE: 32768>
    Constant from pci.h

libpci._macros.PCI_HT_SW_SB_LO = <PCI_HT_SW_SB_LO: 0>
    Constant from pci.h

libpci._macros.PCI_BASE_CLASS_COMMUNICATION = <PCI_BASE_CLASS_COMMUNICATION: 7>
    Constant from pci.h

libpci._macros.PCI_AGP_COMMAND_AGP = <PCI_AGP_COMMAND_AGP: 256>
    Constant from pci.h

libpci._macros.PCI_HT_AM40_DMA_PBASE0 = <PCI_HT_AM40_DMA_PBASE0: 12>
    Constant from pci.h

libpci._macros.PCI_CLASS_SATELLITE_TV = <PCI_CLASS_SATELLITE_TV: 3840>
    Constant from pci.h

libpci._macros.PCI_CLASS_NETWORK_TOKEN_RING = <PCI_CLASS_NETWORK_TOKEN_RING: 513>
    Constant from pci.h

libpci._macros.PCI_HT_SEC_CMD_CS = <PCI_HT_SEC_CMD_CS: 128>
    Constant from pci.h

libpci._macros.PCI_CLASS_MULTIMEDIA_PHONE = <PCI_CLASS_MULTIMEDIA_PHONE: 1026>
    Constant from pci.h

libpci._macros.PCI_HT_AM40_DMA_CTR0 = <PCI_HT_AM40_DMA_CTR0: 15>
    Constant from pci.h

libpci._macros.PCI_CLASS_NETWORK_FDDI = <PCI_CLASS_NETWORK_FDDI: 514>
    Constant from pci.h

libpci._macros.PCI_CLASS_SYSTEM_RTC = <PCI_CLASS_SYSTEM_RTC: 2051>
    Constant from pci.h

libpci._macros.PCI_HT_SEC_CMD_HIECE = <PCI_HT_SEC_CMD_HIECE: 2048>
    Constant from pci.h

libpci._macros.PCI_EVNDR_HEADER = <PCI_EVNDR_HEADER: 4>
    Constant from pci.h

libpci._macros.PCI_CAP_ID_PCIX = <PCI_CAP_ID_PCIX: 7>
    Constant from pci.h

libpci._macros.PCI_MSI_FLAGS_64BIT = <PCI_MSI_FLAGS_64BIT: 128>
    Constant from pci.h

libpci._macros.PCI_EXP_RTSTA_PME_REQID = <PCI_EXP_RTSTA_PME_REQID: 65535>
    Constant from pci.h

libpci._macros.PCI_HT_SW_SWINF_BLRIDX = <PCI_HT_SW_SWINF_BLRIDX: 258048>
    Constant from pci.h

libpci._macros.PCI_BASE_CLASS_MEMORY = <PCI_BASE_CLASS_MEMORY: 5>
    Constant from pci.h

libpci._macros.PCI_CB_IO_BASE_0 = <PCI_CB_IO_BASE_0: 44>
    Constant from pci.h
```

```
libpci._macros.PCI_IO_RANGE_TYPE_16 = <PCI_IO_RANGE_TYPE_16: 0>
    Constant from pci.h
libpci._macros.PCI_BASE_ADDRESS_3 = <PCI_BASE_ADDRESS_3: 28>
    Constant from pci.h
libpci._macros.PCI_CLASS_SERIAL_FIREWIRE = <PCI_CLASS_SERIAL_FIREWIRE: 3072>
    Constant from pci.h
libpci._macros.PCI_HT_LFCAP_1600 = <PCI_HT_LFCAP_1600: 512>
    Constant from pci.h
libpci._macros.PCI_HT_LCNF_DFO = <PCI_HT_LCNF_DFO: 128>
    Constant from pci.h
libpci._macros.PCI_CLASS_INPUT_MOUSE = <PCI_CLASS_INPUT_MOUSE: 2306>
    Constant from pci.h
libpci._macros.PCI_EXP_SLTCTL_CMDC = <PCI_EXP_SLTCTL_CMDC: 16>
    Constant from pci.h
libpci._macros.PCI_HT_LCTR_CFLE = <PCI_HT_LCTR_CFLE: 2>
    Constant from pci.h
libpci._macros.PCI_EXP_LNKCTL2_MOD_CMPLNC = <PCI_EXP_LNKCTL2_MOD_CMPLNC: 1024>
    Constant from pci.h
libpci._macros.PCI_MSI_FLAGS = <PCI_MSI_FLAGS: 2>
    Constant from pci.h
libpci._macros.PCI_HT_AM_CMD_MT = <PCI_HT_AM_CMD_MT: 1536>
    Constant from pci.h
libpci._macros.PCI_EXP_DEVCAP_PWR_VAL = <PCI_EXP_DEVCAP_PWR_VAL: 66846720>
    Constant from pci.h
libpci._macros.PCI_AGP_COMMAND_CAL_MASK = <PCI_AGP_COMMAND_CAL_MASK: 7168>
    Constant from pci.h
libpci._macros.PCI_EXP_DEVCTL_RELAXED = <PCI_EXP_DEVCTL_RELAXED: 16>
    Constant from pci.h
libpci._macros.PCI_CLASS_INTELLIGENT_I2O = <PCI_CLASS_INTELLIGENT_I2O: 3584>
    Constant from pci.h
libpci._macros.PCI_HT_AM40_DMA_SBASE0 = <PCI_HT_AM40_DMA_SBASE0: 18>
    Constant from pci.h
libpci._macros.PCI_PCIX_STATUS_DEVICE = <PCI_PCIX_STATUS_DEVICE: 248>
    Constant from pci.h
libpci._macros.PCI_HT_LFRER_600 = <PCI_HT_LFRER_600: 4>
    Constant from pci.h
libpci._macros.PCI_HT_SW_PC_RDWT = <PCI_HT_SW_PC_RDWT: 11>
    Constant from pci.h
libpci._macros.PCI_STATUS_DETECTED_PARITY = <PCI_STATUS_DETECTED_PARITY: 32768>
    Constant from pci.h
libpci._macros.PCI_HT_LCTR_EOC = <PCI_HT_LCTR_EOC: 64>
    Constant from pci.h
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libpci._macros.PCI_PCIX_STATUS_DESIGNED_MAX_CUMULATIVE_READ_SIZE = <PCI_PCIX_STATUS_DESIGNED_MAX_CUMULATIVE_READ_SIZE: 1>
Constant from pci.h

libpci._macros.PCI_CLASS_MULTIMEDIA_AUDIO_DEV = <PCI_CLASS_MULTIMEDIA_AUDIO_DEV: 1027>
Constant from pci.h

libpci._macros.PCI_AGP_STATUS = <PCI_AGP_STATUS: 4>
Constant from pci.h

libpci._macros.PCI_IOV_CTRL_MSE = <PCI_IOV_CTRL_MSE: 8>
Constant from pci.h

libpci._macros.PCI_PREF_RANGE_TYPE_64 = <PCI_PREF_RANGE_TYPE_64: 1>
Constant from pci.h

libpci._macros.PCI_ERR_CAP = <PCI_ERR_CAP: 24>
Constant from pci.h

libpci._macros.PCI_IO_BASE_UPPER16 = <PCI_IO_BASE_UPPER16: 48>
Constant from pci.h

libpci._macros.PCI_HT_IDC_IDR_PASSPW = <PCI_HT_IDC_IDR_PASSPW: 1073741824>
Constant from pci.h

libpci._macros.PCI_HT_AM_CMD_NDMA = <PCI_HT_AM_CMD_NDMA: 15>
Constant from pci.h

libpci._macros.PCI_HT_SW_BLR_LIM0_LO = <PCI_HT_SW_BLR_LIM0_LO: 2>
Constant from pci.h

libpci._macros.PCI_CB_IO_BASE_0_HI = <PCI_CB_IO_BASE_0_HI: 46>
Constant from pci.h

libpci._macros.PCI_CAP_ID_MSI = <PCI_CAP_ID_MSI: 5>
Constant from pci.h

libpci._macros.PCI_HT_EH_OFLE = <PCI_HT_EH_OFLE: 2>
Constant from pci.h

libpci._macros.PCI_CB_IO_BASE_1_HI = <PCI_CB_IO_BASE_1_HI: 54>
Constant from pci.h

libpci._macros.PCI_ERR_UNC_POISON_TLP = <PCI_ERR_UNC_POISON_TLP: 4096>
Constant from pci.h

libpci._macros.PCI_AGP_COMMAND_ARQSZ_MASK = <PCI_AGP_COMMAND_ARQSZ_MASK: 57344>
Constant from pci.h

libpci._macros.PCI_EXP_SLTCTL_INTERLOCK = <PCI_EXP_SLTCTL_INTERLOCK: 2048>
Constant from pci.h

libpci._macros.PCI_ATS_CTRL = <PCI_ATS_CTRL: 6>
Constant from pci.h

libpci._macros.PCI_CAP_ID_SSVID = <PCI_CAP_ID_SSVID: 13>
Constant from pci.h

libpci._macros.PCI_CLASS_BRIDGE_NUBUS = <PCI_CLASS_BRIDGE_NUBUS: 1542>
Constant from pci.h

libpci._macros.PCI_AGP_STATUS_HTRANS = <PCI_AGP_STATUS_HTRANS: 64>
Constant from pci.h

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libpci._macros.PCI_HEADER_TYPE = <PCI_HEADER_TYPE: 14>
    Constant from pci.h

libpci._macros.PCI_CLASS_BRIDGE_HOST = <PCI_CLASS_BRIDGE_HOST: 1536>
    Constant from pci.h

libpci._macros.PCI_PCIX_STATUS = <PCI_PCIX_STATUS: 4>
    Constant from pci.h

libpci._macros.PCI_CLASS_CRYPT_ENTERTAINMENT = <PCI_CLASS_CRYPT_ENTERTAINMENT: 4112>
    Constant from pci.h

libpci._macros.PCI_EXP_LINKSTA2_EQU_PHASE3 = <PCI_EXP_LINKSTA2_EQU_PHASE3: 16>
    Constant from pci.h

libpci._macros.PCI_HT_RM_CTR_FSS = <PCI_HT_RM_CTR_FSS: 8>
    Constant from pci.h

libpci._macros.PCI_EXP_DEVCAP_FLRESET = <PCI_EXP_DEVCAP_FLRESET: 268435456>
    Constant from pci.h

libpci._macros.PCI_EXP_LNKSTA_TRAIN = <PCI_EXP_LNKSTA_TRAIN: 2048>
    Constant from pci.h

libpci._macros.PCI_CLASS_BRIDGE_EISA = <PCI_CLASS_BRIDGE_EISA: 1538>
    Constant from pci.h

libpci._macros.PCI_PCIX_BRIDGE_SIZEOF = <PCI_PCIX_BRIDGE_SIZEOF: 12>
    Constant from pci.h

libpci._macros.PCI_REVISION_ID = <PCI_REVISION_ID: 8>
    Constant from pci.h

libpci._macros.PCI_CB_MEMORY_LIMIT_1 = <PCI_CB_MEMORY_LIMIT_1: 40>
    Constant from pci.h

libpci._macros.PCI_PM_CAP_D2 = <PCI_PM_CAP_D2: 1024>
    Constant from pci.h

libpci._macros.PCI_HT_PRI_MBU = <PCI_HT_PRI_MBU: 24>
    Constant from pci.h

libpci._macros.PCI_HT_RM_CTR_ROLNEN = <PCI_HT_RM_CTR_ROLNEN: 4>
    Constant from pci.h

libpci._macros.PCI_AGP_STATUS_RATE2 = <PCI_AGP_STATUS_RATE2: 2>
    Constant from pci.h

libpci._macros.PCI_HT_PRI_LCTR1 = <PCI_HT_PRI_LCTR1: 8>
    Constant from pci.h

libpci._macros.PCI_HT_AM_CMD = <PCI_HT_AM_CMD: 2>
    Constant from pci.h

libpci._macros.PCI_ERR_COR_BAD_DLLP = <PCI_ERR_COR_BAD_DLLP: 128>
    Constant from pci.h

libpci._macros.PCI_EXP_LNKCAP_DLLA = <PCI_EXP_LNKCAP_DLLA: 1048576>
    Constant from pci.h

libpci._macros.PCI_HT_AM_SBW_CTR_NCOH = <PCI_HT_AM_SBW_CTR_NCOH: 2>
    Constant from pci.h
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libpci._macros.PCI_EXP_DEVSTA_URD = <PCI_EXP_DEVSTA_URD: 8>
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libpci._macros.PCI_EXP_DEVSTA_NFED = <PCI_EXP_DEVSTA_NFED: 2>
    Constant from pci.h

libpci._macros.PCI_EXP_FLAGS_TYPE = <PCI_EXP_FLAGS_TYPE: 240>
    Constant from pci.h

libpci._macros.PCI_PCIX_BRIDGE_SEC_STATUS_CLOCK_FREQ = <PCI_PCIX_BRIDGE_SEC_STATUS_CLOCK_FREQ: 1>
    Constant from pci.h

libpci._macros.PCI_AF_CTRL_FLR = <PCI_AF_CTRL_FLR: 1>
    Constant from pci.h

libpci._macros.PCI_IOV_STRIDE = <PCI_IOV_STRIDE: 22>
    Constant from pci.h

libpci._macros.PCI_ACS_CTRL_REQ_RED = <PCI_ACS_CTRL_REQ_RED: 4>
    Constant from pci.h

libpci._macros.PCI_INTERRUPT_PIN = <PCI_INTERRUPT_PIN: 61>
    Constant from pci.h

libpci._macros.PCI_PM_CAP_DSI = <PCI_PM_CAP_DSI: 32>
    Constant from pci.h

libpci._macros.PCI_HT_FTR_64BA = <PCI_HT_FTR_64BA: 16>
    Constant from pci.h

libpci._macros.PCI_EXP_RTCTL_CRSVIS = <PCI_EXP_RTCTL_CRSVIS: 16>
    Constant from pci.h

libpci._macros.PCI_CAP_ID_AGP3 = <PCI_CAP_ID_AGP3: 14>
    Constant from pci.h

libpci._macros.PCI_EVNDR_REGISTERS = <PCI_EVNDR_REGISTERS: 8>
    Constant from pci.h

libpci._macros.PCI_HT_AM64_IDX_DMAPB0 = <PCI_HT_AM64_IDX_DMAPB0: 4>
    Constant from pci.h

libpci._macros.PCI_PCIX_STATUS_SC_DISCARDED = <PCI_PCIX_STATUS_SC_DISCARDED: 262144>
    Constant from pci.h

libpci._macros.PCI_HT_VCS_L1EN = <PCI_HT_VCS_L1EN: 5>
    Constant from pci.h

libpci._macros.PCI_PREF_RANGE_TYPE_32 = <PCI_PREF_RANGE_TYPE_32: 0>
    Constant from pci.h

libpci._macros.PCI_AGP_RFU = <PCI_AGP_RFU: 3>
    Constant from pci.h

libpci._macros.PCI_HT_DR_DATA = <PCI_HT_DR_DATA: 8>
    Constant from pci.h

libpci._macros.PCI_PM_PPB_B2_B3 = <PCI_PM_PPB_B2_B3: 64>
    Constant from pci.h

libpci._macros.PCI_EXP_LNKCTL2_CMPLNC = <PCI_EXP_LNKCTL2_CMPLNC: 16>
    Constant from pci.h
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libpci._macros.PCI_HT_SW_SBD = <PCI_HT_SW_SBD: 20>
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libpci._macros.PCI_CLASS_DISPLAY_3D = <PCI_CLASS_DISPLAY_3D: 770>
    Constant from pci.h
libpci._macros.PCI_EXP_DEVCAP_PWR_IND = <PCI_EXP_DEVCAP_PWR_IND: 16384>
    Constant from pci.h
libpci._macros.PCI_ACS_CAP_VALID = <PCI_ACS_CAP_VALID: 1>
    Constant from pci.h
libpci._macros.PCI_HT_SW_PCD = <PCI_HT_SW_PCD: 12>
    Constant from pci.h
libpci._macros.PCI_HT_IDC_IDX_IDR = <PCI_HT_IDC_IDX_IDR: 16>
    Constant from pci.h
libpci._macros.PCI_CLASS_COMMUNICATION_SERIAL = <PCI_CLASS_COMMUNICATION_SERIAL: 1792>
    Constant from pci.h
libpci._macros.PCI_CB_BRIDGE_CTL_SERR = <PCI_CB_BRIDGE_CTL_SERR: 2>
    Constant from pci.h
libpci._macros.PCI_EXP_DEVSTA = <PCI_EXP_DEVSTA: 10>
    Constant from pci.h
libpci._macros.PCI_HT_CMD_TYP_IDC = <PCI_HT_CMD_TYP_IDC: 32768>
    Constant from pci.h
libpci._macros.PCI_EXP_DEVCAP_RBE = <PCI_EXP_DEVCAP_RBE: 32768>
    Constant from pci.h
libpci._macros.PCI_HT_AM64_IDX = <PCI_HT_AM64_IDX: 4>
    Constant from pci.h
libpci._macros.PCI_HT_ECSA_ADDR_DEV = <PCI_HT_ECSA_ADDR_DEV: 987136>
    Constant from pci.h
libpci._macros.PCI_EXT_CAP_ID_RCECOLL = <PCI_EXT_CAP_ID_RCECOLL: 7>
    Constant from pci.h
libpci._macros.PCI_HT_LCTR_LKFAIL = <PCI_HT_LCTR_LKFAIL: 16>
    Constant from pci.h
libpci._macros.PCI_AF_CAP_TP = <PCI_AF_CAP_TP: 1>
    Constant from pci.h
libpci._macros.PCI_HT_VCS_NFCBD = <PCI_HT_VCS_NFCBD: 12>
    Constant from pci.h
libpci._macros.PCI_EXP_DEVCAP2 = <PCI_EXP_DEVCAP2: 36>
    Constant from pci.h
libpci._macros.PCI_CAP_ID_HT = <PCI_CAP_ID_HT: 8>
    Constant from pci.h
libpci._macros.PCI_COMMAND_FAST_BACK = <PCI_COMMAND_FAST_BACK: 512>
    Constant from pci.h
libpci._macros.PCI_CAP_FLAGS = <PCI_CAP_FLAGS: 2>
    Constant from pci.h
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libpci._macros.PCI_BASE_ADDRESS_0 = <PCI_BASE_ADDRESS_0: 16>
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libpci._macros.PCI_HT_IDC_IDX_LINT = <PCI_HT_IDC_IDX_LINT: 1>
    Constant from pci.h

libpci._macros.PCI_CAP_SIZEOF = <PCI_CAP_SIZEOF: 4>
    Constant from pci.h

libpci._macros.PCI_EXP_TYPE_PCIE_BRIDGE = <PCI_EXP_TYPE_PCIE_BRIDGE: 8>
    Constant from pci.h

libpci._macros.PCI_VPD_ADDR_F = <PCI_VPD_ADDR_F: 32768>
    Constant from pci.h

libpci._macros.PCI_STATUS_CAP_LIST = <PCI_STATUS_CAP_LIST: 16>
    Constant from pci.h

libpci._macros.PCI_INTERRUPT_LINE = <PCI_INTERRUPT_LINE: 60>
    Constant from pci.h

libpci._macros.PCI_ERR_CAP_ECRC_GENE = <PCI_ERR_CAP_ECRC_GENE: 64>
    Constant from pci.h

libpci._macros.PCI_AGP_STATUS_ARQSZ_MASK = <PCI_AGP_STATUS_ARQSZ_MASK: 57344>
    Constant from pci.h

libpci._macros.PCI_EXP_SLTSTA2 = <PCI_EXP_SLTSTA2: 58>
    Constant from pci.h

libpci._macros.PCI_HT_IDC_SIZEOF = <PCI_HT_IDC_SIZEOF: 8>
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libpci._macros.PCI_PREF_MEMORY_LIMIT = <PCI_PREF_MEMORY_LIMIT: 38>
    Constant from pci.h

libpci._macros.PCI_IOV_BAR_BASE = <PCI_IOV_BAR_BASE: 36>
    Constant from pci.h

libpci._macros.PCI_PM_PPB_EXTENSIONS = <PCI_PM_PPB_EXTENSIONS: 6>
    Constant from pci.h

libpci._macros.PCI_CLASS_SATELLITE_VOICE = <PCI_CLASS_SATELLITE_VOICE: 3843>
    Constant from pci.h

libpci._macros.PCI_HT_LFRER_300 = <PCI_HT_LFRER_300: 1>
    Constant from pci.h

libpci._macros.PCI_HT_LFCAP_1200 = <PCI_HT_LFCAP_1200: 128>
    Constant from pci.h

libpci._macros.PCI_EXT_CAP_ID_RCLINK = <PCI_EXT_CAP_ID_RCLINK: 5>
    Constant from pci.h

libpci._macros.PCI_CLASS_STORAGE_SATA = <PCI_CLASS_STORAGE_SATA: 262>
    Constant from pci.h

libpci._macros.PCI_HT_LCNF_LW_NC = <PCI_HT_LCNF_LW_NC: 7>
    Constant from pci.h

libpci._macros.PCI_CLASS_PROCESSOR_CO = <PCI_CLASS_PROCESSOR_CO: 2880>
    Constant from pci.h
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libpci._macros.PCI_BRIDGE_CTL_SERR = <PCI_BRIDGE_CTL_SERR: 2>
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libpci._macros.PCI_PM_CAP_D1 = <PCI_PM_CAP_D1: 512>
    Constant from pci.h

libpci._macros.PCI_CLASS_DISPLAY_OTHER = <PCI_CLASS_DISPLAY_OTHER: 896>
    Constant from pci.h

libpci._macros.PCI_AGP_STATUS_RATE4 = <PCI_AGP_STATUS_RATE4: 4>
    Constant from pci.h

libpci._macros.PCI_MSI_ADDRESS_LO = <PCI_MSI_ADDRESS_LO: 4>
    Constant from pci.h

libpci._macros.PCI_HT_LCNF_DFIE = <PCI_HT_LCNF_DFIE: 2048>
    Constant from pci.h

libpci._macros.PCI_PCIX_STATUS_266MHZ = <PCI_PCIX_STATUS_266MHZ: 1073741824>
    Constant from pci.h

libpci._macros.PCI_VENDOR_ID_COMPAQ = <PCI_VENDOR_ID_COMPAQ: 3601>
    Constant from pci.h

libpci._macros.PCI_HT_DR_CMD_IDX = <PCI_HT_DR_CMD_IDX: 496>
    Constant from pci.h

libpci._macros.PCI_IO_BASE = <PCI_IO_BASE: 28>
    Constant from pci.h

libpci._macros.PCI_CAP_LIST_NEXT = <PCI_CAP_LIST_NEXT: 1>
    Constant from pci.h

libpci._macros.PCI_HT_LFCAP_1000 = <PCI_HT_LFCAP_1000: 64>
    Constant from pci.h

libpci._macros.PCI_EXP_LINKSTA2_EQU_PHASE2 = <PCI_EXP_LINKSTA2_EQU_PHASE2: 8>
    Constant from pci.h

libpci._macros.PCI_BRIDGE_CTL_VGA = <PCI_BRIDGE_CTL_VGA: 8>
    Constant from pci.h

libpci._macros.PCI_ROM_ADDRESS_ENABLE = <PCI_ROM_ADDRESS_ENABLE: 1>
    Constant from pci.h

libpci._macros.PCI_HT_SW_PMASK = <PCI_HT_SW_PMASK: 4>
    Constant from pci.h

libpci._macros.PCI_EXP_SLTCAP_MRL = <PCI_EXP_SLTCAP_MRL: 4>
    Constant from pci.h

libpci._macros.PCI_VC_RES_CAP = <PCI_VC_RES_CAP: 16>
    Constant from pci.h

libpci._macros.PCI_COMMAND_DISABLE_INTx = <PCI_COMMAND_DISABLE_INTx: 1024>
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libpci._macros.PCI_EXP_SLTSTA_PRSD = <PCI_EXP_SLTSTA_PRSD: 8>
    Constant from pci.h

libpci._macros.PCI_PCIX_STATUS_64BIT = <PCI_PCIX_STATUS_64BIT: 65536>
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libpci._macros.PCI_EXP_LINKSTA2_EQU_PHASE1 = <PCI_EXP_LINKSTA2_EQU_PHASE1: 4>
Constant from pci.h

libpci._macros.PCI_BRIDGE_CTL_MASTER_ABORT = <PCI_BRIDGE_CTL_MASTER_ABORT: 32>
Constant from pci.h

libpci._macros.PCI_CLASS_PROCESSOR_PENTIUM = <PCI_CLASS_PROCESSOR_PENTIUM: 2818>
Constant from pci.h

libpci._macros.PCI_EXT_CAP_ID_ACS = <PCI_EXT_CAP_ID_ACS: 13>
Constant from pci.h

libpci._macros.PCI_HT_EH_EOCNFE = <PCI_HT_EH_EOCNFE: 4096>
Constant from pci.h

libpci._macros.PCI_EXP_TYPE_ROOT_PORT = <PCI_EXP_TYPE_ROOT_PORT: 4>
Constant from pci.h

libpci._macros.PCI_LATENCY_TIMER = <PCI_LATENCY_TIMER: 13>
Constant from pci.h

libpci._macros.PCI_HT_DR_IDX_BASE_LO = <PCI_HT_DR_IDX_BASE_LO: 0>
Constant from pci.h

libpci._macros.PCI_PCIX_BRIDGE_STATUS_SPLIT_REQUEST_DELAYED = <PCI_PCIX_BRIDGE_STATUS_SPLIT_>
Constant from pci.h

libpci._macros.PCI_HT_PRI_CMD_UC = <PCI_HT_PRI_CMD_UC: 992>
Constant from pci.h

libpci._macros.PCI_HT_IDC_IDR_II_2 = <PCI_HT_IDC_IDR_II_2: 268435484>
Constant from pci.h

libpci._macros.PCI_AGP_STATUS_CAL_MASK = <PCI_AGP_STATUS_CAL_MASK: 7168>
Constant from pci.h

libpci._macros.PCI_HT_LCNF_LW_16B = <PCI_HT_LCNF_LW_16B: 1>
Constant from pci.h

libpci._macros.PCI_HT_VCS_SINT = <PCI_HT_VCS_SINT: 9>
Constant from pci.h

libpci._macros.PCI_HT_CMD_TYP_HI = <PCI_HT_CMD_TYP_HI: 57344>
Constant from pci.h

libpci._macros.PCI_EXP_TYPE_ENDPOINT = <PCI_EXP_TYPE_ENDPOINT: 0>
Constant from pci.h

libpci._macros.PCI_SSVID_DEVICE = <PCI_SSVID_DEVICE: 6>
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libpci._macros.PCI_CLASS_WIRELESS_OTHER = <PCI_CLASS_WIRELESS_OTHER: 3456>
Constant from pci.h

libpci._macros.PCI_EXP_SLTCAP_PWRC = <PCI_EXP_SLTCAP_PWRC: 2>
Constant from pci.h

libpci._macros.PCI_CLASS_NETWORK_ISDN = <PCI_CLASS_NETWORK_ISDN: 516>
Constant from pci.h

libpci._macros.PCI_HT_SW_SWINF_HIDE = <PCI_HT_SW_SWINF_HIDE: 524288>
Constant from pci.h

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libpci._macros.PCI_BASE_CLASS_DISPLAY = <PCI_BASE_CLASS_DISPLAY: 3>
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libpci._macros.PCI_HT_DR_EN = <PCI_HT_DR_EN: 4>
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libpci._macros.PCI_PCIX_BRIDGE_SEC_STATUS = <PCI_PCIX_BRIDGE_SEC_STATUS: 2>
    Constant from pci.h
libpci._macros.PCI_BRIDGE_CTL_PRI_DISCARD_TIMER = <PCI_BRIDGE_CTL_PRI_DISCARD_TIMER: 256>
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libpci._macros.PCI_BASE_ADDRESS_1 = <PCI_BASE_ADDRESS_1: 20>
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libpci._macros.PCI_HT_SW_BLR_BASE0_LO = <PCI_HT_SW_BLR_BASE0_LO: 0>
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libpci._macros.PCI_ARI_CAP_MFVC = <PCI_ARI_CAP_MFVC: 1>
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libpci._macros.PCI_HT_SEC_RID = <PCI_HT_SEC_RID: 8>
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libpci._macros.PCI_EXP_FLAGS_IRQ = <PCI_EXP_FLAGS_IRQ: 15872>
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libpci._macros.PCI_CLASS_SYSTEM_OTHER = <PCI_CLASS_SYSTEM_OTHER: 2176>
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libpci._macros.PCI_HT_AM_CMD_MT_40B = <PCI_HT_AM_CMD_MT_40B: 0>
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libpci._macros.PCI_CLASS_MULTIMEDIA_AUDIO = <PCI_CLASS_MULTIMEDIA_AUDIO: 1025>
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libpci._macros.PCI_PCIX_BRIDGE_SEC_STATUS_UNEXPECTED_SC = <PCI_PCIX_BRIDGE_SEC_STATUS_UNEXP
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libpci._macros.PCI_AGP_STATUS_64BIT = <PCI_AGP_STATUS_64BIT: 32>
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libpci._macros.PCI_EXT_CAP_ID_SRIOV = <PCI_EXT_CAP_ID_SRIOV: 16>
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libpci._macros.PCI_LTR_MAX_NOSNOOP = <PCI_LTR_MAX_NOSNOOP: 6>
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libpci._macros.PCI_VPD_ADDR_MASK = <PCI_VPD_ADDR_MASK: 32767>
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libpci._macros.PCI_IOV_INITIALVF = <PCI_IOV_INITIALVF: 12>
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libpci._macros.PCI_EXP_SLTCTL_HPIE = <PCI_EXP_SLTCTL_HPIE: 32>
    Constant from pci.h
libpci._macros.PCI_HT_PRI_LCNF0 = <PCI_HT_PRI_LCNF0: 6>
    Constant from pci.h

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`libpci._macros.PCI_HT_PRI_BN = <PCI_HT_PRI_BN: 26>`
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`libpci._macros.PCI_HT_ECSA_ADDR_REG = <PCI_HT_ECSA_ADDR_REG: 4092>`
Constant from pci.h

`libpci._macros.PCI_PCIX_BRIDGE_DOWNSTREAM_SPLIT_TRANS_CTRL = <PCI_PCIX_BRIDGE_DOWNSTREAM_S`
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`libpci._macros.PCI_HT_ECSA_ADDR_FUN = <PCI_HT_ECSA_ADDR_FUN: 28672>`
Constant from pci.h

`libpci._macros.PCI_ERR_UNC_MALF_TLP = <PCI_ERR_UNC_MALF_TLP: 262144>`
Constant from pci.h

`libpci._macros.PCI_ACS_CAP_CMPLT_RED = <PCI_ACS_CAP_CMPLT_RED: 8>`
Constant from pci.h

`libpci._macros.PCI_HT_LCTR_TXO = <PCI_HT_LCTR_TXO: 128>`
Constant from pci.h

`libpci._macros.PCI_CAP_ID_SLOTID = <PCI_CAP_ID_SLOTID: 4>`
Constant from pci.h

`libpci._macros.PCI_BASE_CLASS_DOCKING = <PCI_BASE_CLASS_DOCKING: 10>`
Constant from pci.h

`libpci._macros.PCI_EXP_DEVSTA_CED = <PCI_EXP_DEVSTA_CED: 1>`
Constant from pci.h

`libpci._macros.PCI_CLASS_WIRELESS_IRDA = <PCI_CLASS_WIRELESS_IRDA: 3328>`
Constant from pci.h

`libpci._macros.PCI_EXP_SLTSTA_PWRF = <PCI_EXP_SLTSTA_PWRF: 2>`
Constant from pci.h

`libpci._macros.PCI_EXP_SLTCTL = <PCI_EXP_SLTCTL: 24>`
Constant from pci.h

`libpci._macros.PCI_CACHE_LINE_SIZE = <PCI_CACHE_LINE_SIZE: 12>`
Constant from pci.h

`libpci._macros.PCI_ERR_ROOT_SRC = <PCI_ERR_ROOT_SRC: 54>`
Constant from pci.h

`libpci._macros.PCI_MIN_GNT = <PCI_MIN_GNT: 62>`
Constant from pci.h

`libpci._macros.PCI_HT_RM_CNT0 = <PCI_HT_RM_CNT0: 8>`
Constant from pci.h

`libpci._macros.PCI_HEADER_TYPE_BRIDGE = <PCI_HEADER_TYPE_BRIDGE: 1>`
Constant from pci.h

`libpci._macros.PCI_HT_LFRER_500 = <PCI_HT_LFRER_500: 3>`
Constant from pci.h

`libpci._macros.PCI_AGP_SIZEOF = <PCI_AGP_SIZEOF: 12>`
Constant from pci.h

`libpci._macros.PCI_HT_CMD_TYP_HI_SEC = <PCI_HT_CMD_TYP_HI_SEC: 8192>`
Constant from pci.h

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libpci._macros.PCI_HT_SEC_LFRER = <PCI_HT_SEC_LFRER: 9>
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libpci._macros.PCI_VENDOR_ID = <PCI_VENDOR_ID: 0>
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libpci._macros.PCI_AGP_COMMAND_RATE4 = <PCI_AGP_COMMAND_RATE4: 4>
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libpci._macros.PCI_PCIX_BRIDGE_STATUS_SC_OVERRUN = <PCI_PCIX_BRIDGE_STATUS_SC_OVERRUN: 10485>
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libpci._macros.PCI_EXP_DEVCTL_URRE = <PCI_EXP_DEVCTL_URRE: 8>
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libpci._macros.PCI_PCIX_BRIDGE_STATUS_UNEXPECTED_SC = <PCI_PCIX_BRIDGE_STATUS_UNEXPECTED_SC: 6>
    Constant from pci.h

libpci._macros.PCI_COMMAND_VGA_PALETTE = <PCI_COMMAND_VGA_PALETTE: 32>
    Constant from pci.h

libpci._macros.PCI_HT_EH_PFLE = <PCI_HT_EH_PFLE: 1>
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libpci._macros.PCI_HT_PRI_LFCAP1 = <PCI_HT_PRI_LFCAP1: 18>
    Constant from pci.h

libpci._macros.PCI_PCIX_BRIDGE_SEC_STATUS_SPLIT_REQUEST_DELAYED = <PCI_PCIX_BRIDGE_SEC_STATUS_SPLIT_REQUEST_DELAYED: 1>
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libpci._macros.PCI_IO_LIMIT_UPPER16 = <PCI_IO_LIMIT_UPPER16: 50>
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libpci._macros.PCI_HT_IDC_IDR_II_6 = <PCI_HT_IDC_IDR_II_6: 16777152>
    Constant from pci.h

libpci._macros.PCI_RCLINK_LINK_ADDR = <PCI_RCLINK_LINK_ADDR: 8>
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libpci._macros.PCI_MAX_LAT = <PCI_MAX_LAT: 63>
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libpci._macros.PCI_ERR_COR_REP_ROLL = <PCI_ERR_COR_REP_ROLL: 256>
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libpci._macros.PCI_HT_RID_MAJ = <PCI_HT_RID_MAJ: 224>
    Constant from pci.h

libpci._macros.PCI_EXP_SLTCAP_HPS = <PCI_EXP_SLTCAP_HPS: 32>
    Constant from pci.h

libpci._macros.PCI_VC_PORT_REG1 = <PCI_VC_PORT_REG1: 4>
    Constant from pci.h

libpci._macros.PCI_CLASS_PROG = <PCI_CLASS_PROG: 9>
    Constant from pci.h

libpci._macros.PCI_PCIX_BRIDGE_SEC_STATUS_RESERVED = <PCI_PCIX_BRIDGE_SEC_STATUS_RESERVED: 6>
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libpci._macros.PCI_ACS_CAP_BLOCK = <PCI_ACS_CAP_BLOCK: 2>
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Constant from pci.h

`libpci._macros.PCI_RCLINK_LINK_DESC = <PCI_RCLINK_LINK_DESC: 0>`
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`libpci._macros.PCI_CLASS_BRIDGE_OTHER = <PCI_CLASS_BRIDGE_OTHER: 1664>`
Constant from pci.h

`libpci._macros.PCI_HT_PRI_FTR = <PCI_HT_PRI_FTR: 16>`
Constant from pci.h

`libpci._macros.PCI_ACS_CTRL = <PCI_ACS_CTRL: 6>`
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`libpci._macros.PCI_HT_LCNF_DFOE = <PCI_HT_LCNF_DFOE: 32768>`
Constant from pci.h

`libpci._macros.PCI_IOV_DID = <PCI_IOV_DID: 26>`
Constant from pci.h

`libpci._macros.PCI_EXP_RTCAP_CRSVIS = <PCI_EXP_RTCAP_CRSVIS: 16>`
Constant from pci.h

`libpci._macros.PCI_EXP_LNKCTL_XSYNCH = <PCI_EXP_LNKCTL_XSYNCH: 128>`
Constant from pci.h

`libpci._macros.PCI_CLASS_SIGNAL_OTHER = <PCI_CLASS_SIGNAL_OTHER: 4480>`
Constant from pci.h

`libpci._macros.PCI_PREF_RANGE_TYPE_MASK = <PCI_PREF_RANGE_TYPE_MASK: 15>`
Constant from pci.h

`libpci._macros.PCI_ACS_CAP_TRANS = <PCI_ACS_CAP_TRANS: 64>`
Constant from pci.h

`libpci._macros.PCI_EXP_RTSTA_PME_PENDING = <PCI_EXP_RTSTA_PME_PENDING: 131072>`
Constant from pci.h

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Constant from pci.h

`libpci._macros.PCI_BASE_CLASS_BRIDGE = <PCI_BASE_CLASS_BRIDGE: 6>`
Constant from pci.h

`libpci._macros.PCI_CB_BRIDGE_CTL_VGA = <PCI_CB_BRIDGE_CTL_VGA: 8>`
Constant from pci.h

`libpci._macros.PCI_ERR_UNC_UNSUP = <PCI_ERR_UNC_UNSUP: 1048576>`
Constant from pci.h

`libpci._macros.PCI_CLASS_STORAGE_RAID = <PCI_CLASS_STORAGE_RAID: 260>`
Constant from pci.h

`libpci._macros.PCI_HT_IDC_IDR_II_5 = <PCI_HT_IDC_IDR_II_5: 268435488>`
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libpci._macros.PCI_HT_AM_SBW_CTR_EN = <PCI_HT_AM_SBW_CTR_EN: 8>
    Constant from pci.h

libpci._macros.PCI_EXP_SLTSTA_PRES = <PCI_EXP_SLTSTA_PRES: 64>
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libpci._macros.PCI_HT_SW_SWINF_EN = <PCI_HT_SW_SWINF_EN: 32>
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libpci._macros.PCI_CLASS_NOT_DEFINED_VGA = <PCI_CLASS_NOT_DEFINED_VGA: 1>
    Constant from pci.h

libpci._macros.PCI_HT_SW_PC_NPDWT = <PCI_HT_SW_PC_NPDWT: 10>
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libpci._macros.PCI_PCIX_BRIDGE_STATUS_RESERVED = <PCI_PCIX_BRIDGE_STATUS_RESERVED: 4290772992>
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libpci._macros.PCI_HEADER_TYPE_CARDBUS = <PCI_HEADER_TYPE_CARDBUS: 2>
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libpci._macros.PCI_EXP_LNKCTL_AUTBWIE = <PCI_EXP_LNKCTL_AUTBWIE: 2048>
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libpci._macros.PCI_HT_PRI_CMD_MH = <PCI_HT_PRI_CMD_MH: 1024>
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libpci._macros.PCI_CLASS_SERIAL_INFINIBAND = <PCI_CLASS_SERIAL_INFINIBAND: 3078>
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libpci._macros.PCI_HT_LFRER_CTLT = <PCI_HT_LFRER_CTLT: 128>
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libpci._macros.PCI_CLASS_COMMUNICATION_MSERIAL = <PCI_CLASS_COMMUNICATION_MSERIAL: 1794>
    Constant from pci.h

libpci._macros.PCI_PCIX_STATUS_FUNCTION = <PCI_PCIX_STATUS_FUNCTION: 7>
    Constant from pci.h

libpci._macros.PCI_BASE_CLASS_SIGNAL = <PCI_BASE_CLASS_SIGNAL: 17>
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libpci._macros.PCI_ERR_COR_BAD_TLP = <PCI_ERR_COR_BAD_TLP: 64>
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Constant from pci.h

libpci._macros.PCI_AF_STATUS_TP = <PCI_AF_STATUS_TP: 1>
Constant from pci.h

libpci._macros.PCI_MEMORY_BASE = <PCI_MEMORY_BASE: 32>
Constant from pci.h

libpci._macros.PCI_HT_RM_SIZEOF = <PCI_HT_RM_SIZEOF: 12>
Constant from pci.h

libpci._macros.PCI_HT_LFRER_ERR = <PCI_HT_LFRER_ERR: 240>
Constant from pci.h

libpci._macros.PCI_EXP_SLTCTL2 = <PCI_EXP_SLTCTL2: 56>
Constant from pci.h

libpci._macros.PCI_HT_LCTR_64B = <PCI_HT_LCTR_64B: 32768>
Constant from pci.h

libpci._macros.PCI_MSIX_MASK = <PCI_MSIX_MASK: 16384>
Constant from pci.h

libpci._macros.PCI_EXP_SLTCAP_ATNB = <PCI_EXP_SLTCAP_ATNB: 1>
Constant from pci.h

libpci._macros.PCI_HT_ECSA_DATA = <PCI_HT_ECSA_DATA: 8>
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libpci._macros.PCI_CLASS_STORAGE_SCSI = <PCI_CLASS_STORAGE_SCSI: 256>
Constant from pci.h

libpci._macros.PCI_PM_CTRL_PME_STATUS = <PCI_PM_CTRL_PME_STATUS: 32768>
Constant from pci.h

libpci._macros.PCI_ERR_UNCOR_MASK = <PCI_ERR_UNCOR_MASK: 8>
Constant from pci.h

libpci._macros.PCI_PCIX_STATUS_UNEXPECTED_SC = <PCI_PCIX_STATUS_UNEXPECTED_SC: 524288>
Constant from pci.h

libpci._macros.PCI_BRIDGE_CTL_BUS_RESET = <PCI_BRIDGE_CTL_BUS_RESET: 64>
Constant from pci.h

libpci._macros.PCI_PM_CAP_PME_CLOCK = <PCI_PM_CAP_PME_CLOCK: 8>
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libpci._macros.PCI_PM_CAP_PME_D0 = <PCI_PM_CAP_PME_D0: 2048>
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libpci._macros.PCI_CLASS_BRIDGE_CARDBUS = <PCI_CLASS_BRIDGE_CARDBUS: 1543>
Constant from pci.h

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libpci._macros.PCI_PCIX_COMMAND = <PCI_PCIX_COMMAND: 2>
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libpci._macros.PCI_ERR_UNCOR_STATUS = <PCI_ERR_UNCOR_STATUS: 4>
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libpci._macros.PCI_EXP_SLTCAP_PWR_VAL = <PCI_EXP_SLTCAP_PWR_VAL: 32640>
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libpci._macros.PCI_AGP_COMMAND_SBA = <PCI_AGP_COMMAND_SBA: 512>
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libpci._macros.PCI_AGP_COMMAND_64BIT = <PCI_AGP_COMMAND_64BIT: 32>
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libpci._macros.PCI_COMMAND_PARITY = <PCI_COMMAND_PARITY: 64>
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libpci._macros.PCI_CAP_ID_CHSWP = <PCI_CAP_ID_CHSWP: 6>
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libpci._macros.PCI_EXP_SLTCAP_NOCMDCOMP = <PCI_EXP_SLTCAP_NOCMDCOMP: 262144>
    Constant from pci.h

libpci._macros.PCI_CB_BRIDGE_CTL_16BIT_INT = <PCI_CB_BRIDGE_CTL_16BIT_INT: 128>
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libpci._macros.PCI_HT_SW_BLR_BASE0_HI = <PCI_HT_SW_BLR_BASE0_HI: 1>
    Constant from pci.h

libpci._macros.PCI_HT_SW_CMD_VIBFT = <PCI_HT_SW_CMD_VIBFT: 512>
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libpci._macros.PCI_EXP_DEVCAP_L0S = <PCI_EXP_DEVCAP_L0S: 448>
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libpci._macros.PCI_CLASS_STORAGE_SAS = <PCI_CLASS_STORAGE_SAS: 263>
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libpci._macros.PCI_HT_RM_CTR_FSER = <PCI_HT_RM_CTR_FSER: 2>
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libpci._macros.PCI_BASE_CLASS_INTELLIGENT = <PCI_BASE_CLASS_INTELLIGENT: 14>
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libpci._macros.PCI_HT_SW_PC_PCR = <PCI_HT_SW_PC_PCR: 0>
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libpci._macros.PCI_HT_IDC_IDR_WEOI = <PCI_HT_IDC_IDR_WEOI: 2147483648>
Constant from pci.h

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libpci._macros.PCI_HT_CMD_TYP = <PCI_HT_CMD_TYP: 63488>
Constant from pci.h

libpci._macros.PCI_HT_RM_CTR_RETFEN = <PCI_HT_RM_CTR_RETFEN: 32>
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libpci._macros.PCI_STATUS_SIG_SYSTEM_ERROR = <PCI_STATUS_SIG_SYSTEM_ERROR: 16384>
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libpci._macros.PCI_HT_AM64_SIZEOF = <PCI_HT_AM64_SIZEOF: 16>
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libpci._macros.PCI_MSI_DATA_64 = <PCI_MSI_DATA_64: 12>
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libpci._macros.PCI_HT_EH_CF = <PCI_HT_EH_CF: 256>
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libpci._macros.PCI_EXP_TYPE_LEG_END = <PCI_EXP_TYPE_LEG_END: 1>
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libpci._macros.PCI_HT_PRI_CMD_DD = <PCI_HT_PRI_CMD_DD: 2048>
Constant from pci.h

libpci._macros.PCI_MSIX_PBA = <PCI_MSIX_PBA: 8>
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Constant from pci.h

libpci._macros.PCI_HT_LFCAP_1400 = <PCI_HT_LFCAP_1400: 256>
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libpci._macros.PCI_ERR_COR_MASK = <PCI_ERR_COR_MASK: 20>
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libpci._macros.PCI_BRIDGE_CTL_NO_ISA = <PCI_BRIDGE_CTL_NO_ISA: 4>
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libpci._macros.PCI_EXP_DEVCAP_PHANTOM = <PCI_EXP_DEVCAP_PHANTOM: 24>
Constant from pci.h

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libpci._macros.PCI_EXP_LNKCTL2_SPEED_DIS = <PCI_EXP_LNKCTL2_SPEED_DIS: 32>
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libpci._macros.PCI_HT_AM_SBW_CTR_ISOC = <PCI_HT_AM_SBW_CTR_ISOC: 4>
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libpci._macros.PCI_SECONDARY_BUS = <PCI_SECONDARY_BUS: 25>
    Constant from pci.h

libpci._macros.PCI_PCIX_BRIDGE_STATUS_FUNCTION = <PCI_PCIX_BRIDGE_STATUS_FUNCTION: 7>
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libpci._macros.PCI_HT_AM64_IDX_PBNPW = <PCI_HT_AM64_IDX_PBNPW: 2>
    Constant from pci.h

libpci._macros.PCI_ERR_UNC_FCP = <PCI_ERR_UNC_FCP: 8192>
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libpci._macros.PCI_HT_AM64_IDX_SBNPW = <PCI_HT_AM64_IDX_SBNPW: 0>
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libpci._macros.PCI_HT_SW_SWINF_SBIDX = <PCI_HT_SW_SWINF_SBIDX: 8192>
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libpci._macros.PCI_CLASS_BRIDGE_PCMCIA = <PCI_CLASS_BRIDGE_PCMCIA: 1541>
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libpci._macros.PCI_HT_AM64_IDX_SBPW = <PCI_HT_AM64_IDX_SBPW: 1>
    Constant from pci.h

libpci._macros.PCI_HT_CMD_TYP_DR = <PCI_HT_CMD_TYP_DR: 45056>
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libpci._macros.PCI_EXP_LNKCAP2 = <PCI_EXP_LNKCAP2: 44>
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`libpci._macros.PCI_SUBORDINATE_BUS = <PCI_SUBORDINATE_BUS: 26>`
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Constant from pci.h

`libpci._macros.PCI_CLASS_BRIDGE_MC = <PCI_CLASS_BRIDGE_MC: 1539>`
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`libpci._macros.PCI_HT_LFRER_1600 = <PCI_HT_LFRER_1600: 9>`
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`libpci._macros.PCI_EXP_SLTCTL_ATNI = <PCI_EXP_SLTCTL_ATNI: 192>`
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Constant from pci.h

`libpci._macros.PCI_HT_EH_RFE = <PCI_HT_EH_RFE: 32>`
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`libpci._macros.PCI_PWR_DATA = <PCI_PWR_DATA: 8>`
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Constant from pci.h

`libpci._macros.PCI_HT_LFCAP_400 = <PCI_HT_LFCAP_400: 4>`
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`libpci._macros.PCI_ERR_ROOT_STATUS = <PCI_ERR_ROOT_STATUS: 48>`
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libpci._macros.PCI_CB_BRIDGE_CONTROL = <PCI_CB_BRIDGE_CONTROL: 62>
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libpci._macros.PCI_EXT_CAP_ID_ATS = <PCI_EXT_CAP_ID_ATS: 15>
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libpci._macros.PCI_EXP_DEVCAP_ATN_BUT = <PCI_EXP_DEVCAP_ATN_BUT: 4096>
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libpci._macros.PCI_ACS_CTRL_EGRESS = <PCI_ACS_CTRL_EGRESS: 32>
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libpci._macros.PCI_CLASS_SYSTEM_PIC = <PCI_CLASS_SYSTEM_PIC: 2048>
    Constant from pci.h
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`libpci._macros.PCI_IOV_NUM_BAR = <PCI_IOV_NUM_BAR: 6>`
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`libpci._macros.PCI_MEMORY_RANGE_TYPE_MASK = <PCI_MEMORY_RANGE_TYPE_MASK: 15>`
Constant from pci.h

`libpci._macros.PCI_HT_SEC_MBU = <PCI_HT_SEC_MBU: 20>`
Constant from pci.h

`libpci._macros.PCI_EXP_LNKSTA_DL_ACT = <PCI_EXP_LNKSTA_DL_ACT: 8192>`
Constant from pci.h

`libpci._macros.PCI_ERR_COR_REP_TIMER = <PCI_ERR_COR_REP_TIMER: 4096>`
Constant from pci.h

`libpci._macros.PCI_BASE_ADDRESS_MEM_PREFETCH = <PCI_BASE_ADDRESS_MEM_PREFETCH: 8>`
Constant from pci.h

`libpci._macros.PCI_ACS_CTRL_TRANS = <PCI_ACS_CTRL_TRANS: 64>`
Constant from pci.h

`libpci._macros.PCI_HT_ECSA_SIZEOF = <PCI_HT_ECSA_SIZEOF: 12>`
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`libpci._macros.PCI_HT_PRI_RID = <PCI_HT_PRI_RID: 12>`
Constant from pci.h

`libpci._macros.PCI_CLASS_DOCKING_GENERIC = <PCI_CLASS_DOCKING_GENERIC: 2560>`
Constant from pci.h

`libpci._macros.PCI_HT_DR_IDX_LIMIT_HI = <PCI_HT_DR_IDX_LIMIT_HI: 3>`
Constant from pci.h

`libpci._macros.PCI_EXP_SLTCAP_INTERLOCK = <PCI_EXP_SLTCAP_INTERLOCK: 131072>`
Constant from pci.h

`libpci._macros.PCI_HT_AM64_W_CTR = <PCI_HT_AM64_W_CTR: 15>`
Constant from pci.h

`libpci._macros.PCI_EXP_DEV2_LTR = <PCI_EXP_DEV2_LTR: 1024>`
Constant from pci.h

`libpci._macros.PCI_HT_IDC_LINT = <PCI_HT_IDC_LINT: 16711680>`
Constant from pci.h

`libpci._macros.PCI_PCIX_STATUS_133MHZ = <PCI_PCIX_STATUS_133MHZ: 131072>`
Constant from pci.h

`libpci._macros.PCI_EXP_LNKCAP_L0S = <PCI_EXP_LNKCAP_L0S: 28672>`
Constant from pci.h

`libpci._macros.PCI_EXT_CAP_ID_ARI = <PCI_EXT_CAP_ID_ARI: 14>`
Constant from pci.h

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`libpci._macros.PCI_BIST_CAPABLE = <PCI_BIST_CAPABLE: 128>`
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`libpci._macros.PCI_MSI_MASK_BIT_64 = <PCI_MSI_MASK_BIT_64: 16>`
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libpci._macros.PCI_HT_PRI_LCTR0 = <PCI_HT_PRI_LCTR0: 4>
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libpci._macros.PCI_CLASS_DISPLAY_VGA = <PCI_CLASS_DISPLAY_VGA: 768>
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libpci._macros.PCI_ERR_UNC_ECRC = <PCI_ERR_UNC_ECRC: 524288>
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libpci._macros.PCI_CLASS_SYSTEM_PCI_HOTPLUG = <PCI_CLASS_SYSTEM_PCI_HOTPLUG: 2052>
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libpci._macros.PCI_CB_SEC_STATUS = <PCI_CB_SEC_STATUS: 22>
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libpci._macros.PCI_EXP_SLTCAP_PWRI = <PCI_EXP_SLTCAP_PWRI: 16>
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libpci._macros.PCI_CLASS_MEMORY_OTHER = <PCI_CLASS_MEMORY_OTHER: 1408>
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libpci._macros.PCI_CB_LATENCY_TIMER = <PCI_CB_LATENCY_TIMER: 27>
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libpci._macros.PCI_EXT_CAP_ID_TPH = <PCI_EXT_CAP_ID_TPH: 23>
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libpci._macros.PCI_CLASS_PROCESSOR_POWERPC = <PCI_CLASS_PROCESSOR_POWERPC: 2848>
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libpci._macros.PCI_EXP_SLTCAP_ATNI = <PCI_EXP_SLTCAP_ATNI: 8>
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libpci._macros.PCI_AGP_COMMAND_RQ_MASK = <PCI_AGP_COMMAND_RQ_MASK: 4278190080>
Constant from pci.h

libpci._macros.PCI_HT_SW_PC_NPCT = <PCI_HT_SW_PC_NPCT: 7>
Constant from pci.h

libpci._macros.PCI_HT_EH_CRCNFE = <PCI_HT_EH_CRCNFE: 16384>
Constant from pci.h

libpci._macros.PCI_EXP_LNKSTA_TR_ERR = <PCI_EXP_LNKSTA_TR_ERR: 1024>
Constant from pci.h

libpci._macros.PCI_CB_SUBORDINATE_BUS = <PCI_CB_SUBORDINATE_BUS: 26>
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libpci._macros.PCI_HT_AM_SBW_CTR_COMP = <PCI_HT_AM_SBW_CTR_COMP: 1>
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libpci._macros.PCI_CLASS_STORAGE_IDE = <PCI_CLASS_STORAGE_IDE: 257>
Constant from pci.h

libpci._macros.PCI_PCIX_BRIDGE_STATUS_SC_DISCARDED = <PCI_PCIX_BRIDGE_STATUS_SC_DISCARDED: 2>
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libpci._macros.PCI_BASE_CLASS_WIRELESS = <PCI_BASE_CLASS_WIRELESS: 13>
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libpci._macros.PCI_STATUS_DEVSEL_FAST = <PCI_STATUS_DEVSEL_FAST: 0>
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Constant from pci.h

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Constant from pci.h

libpci._macros.PCI_HT_SEC_FTR_EXTRS = <PCI_HT_SEC_FTR_EXTRS: 256>
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libpci._macros.PCI_EXP_RTSTA_PME_STATUS = <PCI_EXP_RTSTA_PME_STATUS: 65536>
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libpci._macros.PCI_EXP_LNKCAP_SURPRISE = <PCI_EXP_LNKCAP_SURPRISE: 524288>
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libpci._macros.PCI_CLASS_BRIDGE_IB_TO_PCI = <PCI_CLASS_BRIDGE_IB_TO_PCI: 1546>
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libpci._macros.PCI_EXP_DEVCTL_CERE = <PCI_EXP_DEVCTL_CERE: 1>
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libpci._macros.PCI_PCIX_COMMAND_DPERE = <PCI_PCIX_COMMAND_DPERE: 1>
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libpci._macros.PCI_ARI_CTRL_ACS = <PCI_ARI_CTRL_ACS: 2>
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libpci._macros.PCI_HT_FTR_LDTSTOP = <PCI_HT_FTR_LDTSTOP: 2>
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libpci._macros.PCI_HT_VCS_SSUP_15 = <PCI_HT_VCS_SSUP_15: 2>
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libpci._macros.PCI_EXP_DEVCTL = <PCI_EXP_DEVCTL: 8>
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libpci._macros.PCI_CLASS_PROCESSOR_MIPS = <PCI_CLASS_PROCESSOR_MIPS: 2864>
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libpci._macros.PCI_EXP_DEVCTL_READRQ = <PCI_EXP_DEVCTL_READRQ: 28672>
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libpci._macros.PCI_CLASS_MEMORY_FLASH = <PCI_CLASS_MEMORY_FLASH: 1281>
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libpci._macros.PCI_COMMAND_WAIT = <PCI_COMMAND_WAIT: 128>
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libpci._macros.PCI_PM_CTRL_NO_SOFT_RST = <PCI_PM_CTRL_NO_SOFT_RST: 8>
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libpci._macros.PCI_IOV_SYSPS = <PCI_IOV_SYSPS: 32>
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libpci._macros.PCI_CB_BRIDGE_CTL_CB_RESET = <PCI_CB_BRIDGE_CTL_CB_RESET: 64>
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libpci._macros.PCI_HT_SW_PC_RCT = <PCI_HT_SW_PC_RCT: 8>
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libpci._macros.PCI_HT_RM_STS1 = <PCI_HT_RM_STS1: 7>
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libpci._macros.PCI_EXP_SLTCTL_LLCHG = <PCI_EXP_SLTCTL_LLCHG: 4096>
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libpci._macros.PCI_EXP_DEVCAP_PWR_SCL = <PCI_EXP_DEVCAP_PWR_SCL: 201326592>
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libpci._macros.PCI_HT_RM_CTR_AA = <PCI_HT_RM_CTR_AA: 192>
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libpci._macros.PCI_HT_SW_PC_PCT = <PCI_HT_SW_PC_PCT: 6>
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libpci._macros.PCI_PCIX_BRIDGE_STATUS_64BIT = <PCI_PCIX_BRIDGE_STATUS_64BIT: 65536>
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libpci._macros.PCI_STATUS_DEVSEL_MEDIUM = <PCI_STATUS_DEVSEL_MEDIUM: 512>
    Constant from pci.h
libpci._macros.PCI_CLASS_NETWORK_ETHERNET = <PCI_CLASS_NETWORK_ETHERNET: 512>
    Constant from pci.h
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libpci._macros.PCI_PCIX_COMMAND_MAX_MEM_READ_BYTE_COUNT = <PCI_PCIX_COMMAND_MAX_MEM_READ_BYTE_COUNT: 1>
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libpci._macros.PCI_HT_PRI_LFRER1 = <PCI_HT_PRI_LFRER1: 17>
Constant from pci.h

libpci._macros.PCI_CB_BRIDGE_CTL_PREFETCH_MEM1 = <PCI_CB_BRIDGE_CTL_PREFETCH_MEM1: 512>
Constant from pci.h

libpci._macros.PCI_HT_PRI_EH = <PCI_HT_PRI_EH: 22>
Constant from pci.h

libpci._macros.PCI_SID_ESR = <PCI_SID_ESR: 2>
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libpci._macros.PCI_HT_LCNF_LW_4B = <PCI_HT_LCNF_LW_4B: 5>
Constant from pci.h

libpci._macros.PCI_HT_MSIM_CMD_EN = <PCI_HT_MSIM_CMD_EN: 1>
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libpci._macros.PCI_HT_AM_CMD_MT_64B = <PCI_HT_AM_CMD_MT_64B: 512>
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libpci._macros.PCI_MSI_DATA_32 = <PCI_MSI_DATA_32: 8>
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libpci._macros.PCI_ACS_CAP_REQ_RED = <PCI_ACS_CAP_REQ_RED: 4>
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libpci._macros.PCI_CARDBUS_CIS = <PCI_CARDBUS_CIS: 40>
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libpci._macros.PCI_EXP_DEVCTL_PAYLOAD = <PCI_EXP_DEVCTL_PAYLOAD: 224>
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Constant from pci.h

libpci._macros.PCI_ACS_CAP = <PCI_ACS_CAP: 4>
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libpci._macros.PCI_HT_CMD_TYP_SW = <PCI_HT_CMD_TYP_SW: 16384>
Constant from pci.h

libpci._macros.PCI_HT_SEC_CMD_WR = <PCI_HT_SEC_CMD_WR: 1>
Constant from pci.h

libpci._macros.PCI_VC_RES_STATUS = <PCI_VC_RES_STATUS: 26>
Constant from pci.h

libpci._macros.PCI_HT_RM_STS_CNTRL = <PCI_HT_RM_STS_CNTRL: 2>
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libpci._macros.PCI_CLASS_INPUT_OTHER = <PCI_CLASS_INPUT_OTHER: 2432>
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libpci._macros.PCI_HT_PRI_LFRER0 = <PCI_HT_PRI_LFRER0: 13>
Constant from pci.h

libpci._macros.PCI_BRIDGE_CTL_SEC_DISCARD_TIMER = <PCI_BRIDGE_CTL_SEC_DISCARD_TIMER: 512>
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libpci._macros.PCI_EXP_LNKCAP_PORT = <PCI_EXP_LNKCAP_PORT: 4278190080>
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libpci._macros.PCI_EXP_DEVCAP_ATN_IND = <PCI_EXP_DEVCAP_ATN_IND: 8192>
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libpci._macros.PCI_BASE_ADDRESS_MEM_TYPE_MASK = <PCI_BASE_ADDRESS_MEM_TYPE_MASK: 6>
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libpci._macros.PCI_IOV_SUPPS = <PCI_IOV_SUPPS: 28>
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libpci._macros.PCI_CB_IO_LIMIT_0_HI = <PCI_CB_IO_LIMIT_0_HI: 50>
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libpci._macros.PCI_ERR_CAP_ECRC_CHKE = <PCI_ERR_CAP_ECRC_CHKE: 256>
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libpci._macros.PCI_EXP_DEVCTL_FERE = <PCI_EXP_DEVCTL_FERE: 4>
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libpci._macros.PCI_CLASS_COMMUNICATION_PARALLEL = <PCI_CLASS_COMMUNICATION_PARALLEL: 1793>
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libpci._macros.PCI_EXP_DEVCTL_BCRE = <PCI_EXP_DEVCTL_BCRE: 32768>
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libpci._macros.PCI_CLASS_SATELLITE_DATA = <PCI_CLASS_SATELLITE_DATA: 3844>
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libpci._macros.PCI_HT_SEC_LCNF = <PCI_HT_SEC_LCNF: 6>
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libpci._macros.PCI_HT_CMD_TYP_MSIM = <PCI_HT_CMD_TYP_MSIM: 43008>
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libpci._macros.PCI_AGP_STATUS_RQ_MASK = <PCI_AGP_STATUS_RQ_MASK: 4278190080>
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libpci._macros.PCI_HT_SW_SWINF_DP = <PCI_HT_SW_SWINF_DP: 31>
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libpci._macros.PCI_PCIX_STATUS_DEVICE_COMPLEXITY = <PCI_PCIX_STATUS_DEVICE_COMPLEXITY: 10485>
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libpci._macros.PCI_BRIDGE_CTL_DISCARD_TIMER_STATUS = <PCI_BRIDGE_CTL_DISCARD_TIMER_STATUS:
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libpci._macros.PCI_EXT_CAP_ID_VC = <PCI_EXT_CAP_ID_VC: 2>
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libpci._macros.PCI_AGP_COMMAND_RATE2 = <PCI_AGP_COMMAND_RATE2: 2>
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`libpci._macros.PCI_EXP_LNKSTA = <PCI_EXP_LNKSTA: 18>`
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`libpci._macros.PCI_ARI_CTRL = <PCI_ARI_CTRL: 6>`
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`libpci._macros.PCI_HT_EH_SERRFE = <PCI_HT_EH_SERRFE: 128>`
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`libpci._macros.PCI_CLASS_SIGNAL_PERF_CTR = <PCI_CLASS_SIGNAL_PERF_CTR: 4353>`
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`libpci._macros.PCI_HT_LCTR_INIT = <PCI_HT_LCTR_INIT: 32>`
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`libpci._macros.PCI_CLASS_SIGNAL_DPIO = <PCI_CLASS_SIGNAL_DPIO: 4352>`
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`libpci._macros.PCI_EXP_LINKSTA2_EQU_REQ = <PCI_EXP_LINKSTA2_EQU_REQ: 32>`
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`libpci._macros.PCI_BASE_ADDRESS_2 = <PCI_BASE_ADDRESS_2: 24>`
Constant from pci.h

`libpci._macros.PCI_EXP_FLAGS_VERS = <PCI_EXP_FLAGS_VERS: 15>`
Constant from pci.h

`libpci._macros.PCI_CLASS_STORAGE_IPI = <PCI_CLASS_STORAGE_IPI: 259>`
Constant from pci.h

`libpci._macros.PCI_HT_AM40_SBPW = <PCI_HT_AM40_SBPW: 8>`
Constant from pci.h

`libpci._macros.PCI_HT_AM64_W_BASE_LO = <PCI_HT_AM64_W_BASE_LO: 4293918720>`
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`libpci._macros.PCI_HT_RM_CTR0 = <PCI_HT_RM_CTR0: 4>`
Constant from pci.h

`libpci._macros.PCI_EXP_LNKCTL_DISABLE = <PCI_EXP_LNKCTL_DISABLE: 16>`
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`libpci._macros.PCI_STATUS_PARITY = <PCI_STATUS_PARITY: 256>`
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`libpci._macros.PCI_CLASS_SIGNAL_SYNCHRONIZER = <PCI_CLASS_SIGNAL_SYNCHRONIZER: 4368>`
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`libpci._macros.PCI_HT_SW_SWINF_CR = <PCI_HT_SW_SWINF_CR: 64>`
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`libpci._macros.PCI_HT_LCNF_MLWO = <PCI_HT_LCNF_MLWO: 112>`
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libpci._macros.PCI_STATUS_66MHZ = <PCI_STATUS_66MHZ: 32>
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libpci._macros.PCI_ERR_CAP_ECRC_CHK = <PCI_ERR_CAP_ECRC_CHK: 128>
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libpci._macros.PCI_PM_CAP_PME_D1 = <PCI_PM_CAP_PME_D1: 4096>
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libpci._macros.PCI_CLASS_COMMUNICATION_OTHER = <PCI_CLASS_COMMUNICATION_OTHER: 1920>
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libpci._macros.PCI_EXP_LNKCAP = <PCI_EXP_LNKCAP: 12>
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libpci._macros.PCI_PCIX_BRIDGE_STATUS_133MHZ = <PCI_PCIX_BRIDGE_STATUS_133MHZ: 131072>
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libpci._macros.PCI_EXP_DEVCAP_L1 = <PCI_EXP_DEVCAP_L1: 3584>
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libpci._macros.PCI_HT_ECDSA_ADDR_TYPE = <PCI_HT_ECDSA_ADDR_TYPE: 268435456>
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libpci._macros.PCI_HT_AM64_DATA_LO = <PCI_HT_AM64_DATA_LO: 8>
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libpci._macros.PCI_PCIX_COMMAND_ERO = <PCI_PCIX_COMMAND_ERO: 2>
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libpci._macros.PCI_HT_SW_PC_NPDWR = <PCI_HT_SW_PC_NPDWR: 4>
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libpci._macros.PCI_HT_EH_EOCFE = <PCI_HT_EH_EOCFE: 16>
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libpci._macros.PCI_CB_MEMORY_BASE_0 = <PCI_CB_MEMORY_BASE_0: 28>
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libpci._macros.PCI_EXP_LNKCAP_ASPM = <PCI_EXP_LNKCAP_ASPM: 3072>
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libpci._macros.PCI_HT_LFRER_VEND = <PCI_HT_LFRER_VEND: 15>
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`libpci._macros.PCI_HT_LFCAP_800 = <PCI_HT_LFCAP_800: 32>`
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`libpci._macros.PCI_EXP_LNKCTL_HWAUTWD = <PCI_EXP_LNKCTL_HWAUTWD: 512>`
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`libpci._macros.PCI_EXP_LNKCTL_RETRAIN = <PCI_EXP_LNKCTL_RETRAIN: 32>`
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`libpci._macros.PCI_EXP_SLTSTA_CMDC = <PCI_EXP_SLTSTA_CMDC: 16>`
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Constant from pci.h

`libpci._macros.PCI_CLASS_STORAGE_FLOPPY = <PCI_CLASS_STORAGE_FLOPPY: 258>`
Constant from pci.h

`libpci._macros.PCI_STATUS_FAST_BACK = <PCI_STATUS_FAST_BACK: 128>`
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`libpci._macros.PCI_IOV_STATUS = <PCI_IOV_STATUS: 10>`
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`libpci._macros.PCI_CB_BRIDGE_CTL_PREFETCH_MEM0 = <PCI_CB_BRIDGE_CTL_PREFETCH_MEM0: 256>`
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`libpci._macros.PCI_HT_LFRER_200 = <PCI_HT_LFRER_200: 0>`
Constant from pci.h

`libpci._macros.PCI_HT_LFRER_1200 = <PCI_HT_LFRER_1200: 7>`
Constant from pci.h

`libpci._macros.PCI_HT_RM_CTR1 = <PCI_HT_RM_CTR1: 6>`
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`libpci._macros.PCI_HT_EH_PFE = <PCI_HT_EH_PFE: 4>`
Constant from pci.h

`libpci._macros.PCI_HT_RM_STS_SRCV = <PCI_HT_RM_STS_SRCV: 4>`
Constant from pci.h

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Constant from pci.h

`libpci._macros.PCI_HT_SW_SWINF_PCIDX = <PCI_HT_SW_SWINF_PCIDX: 3840>`
Constant from pci.h

`libpci._macros.PCI_RCLINK_LINK_SIZE = <PCI_RCLINK_LINK_SIZE: 16>`
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libpci._macros.PCI_HT_SW_PC_RDWR = <PCI_HT_SW_PC_RDWR: 5>
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libpci._macros.PCI_EXP_SLTSTA = <PCI_EXP_SLTSTA: 26>
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libpci._macros.PCI_EXT_CAP_ID_LTR = <PCI_EXT_CAP_ID_LTR: 24>
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libpci._macros.PCI_CLASS_SYSTEM_TIMER = <PCI_CLASS_SYSTEM_TIMER: 2050>
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libpci._macros.PCI_IOV_FDL = <PCI_IOV_FDL: 18>
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libpci._macros.PCI_HT_VCS_SSUP = <PCI_HT_VCS_SSUP: 10>
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libpci._macros.PCI_CLASS_CRYPT_NETWORK = <PCI_CLASS_CRYPT_NETWORK: 4096>
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libpci._macros.PCI_SUBSYSTEM_VENDOR_ID = <PCI_SUBSYSTEM_VENDOR_ID: 44>
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libpci._macros.PCI_CAP_ID_HOTPLUG = <PCI_CAP_ID_HOTPLUG: 12>
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libpci._macros.PCI_HT_PRI_SIZEOF = <PCI_HT_PRI_SIZEOF: 28>
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libpci._macros.PCI_EXP_LNKSTA2 = <PCI_EXP_LNKSTA2: 50>
    Constant from pci.h

libpci._macros.PCI_AF_CTRL = <PCI_AF_CTRL: 4>
    Constant from pci.h

libpci._macros.PCI_CB_BRIDGE_CTL_POST_WRITES = <PCI_CB_BRIDGE_CTL_POST_WRITES: 1024>
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libpci._macros.PCI_ACS_CTRL_VALID = <PCI_ACS_CTRL_VALID: 1>
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libpci._macros.PCI_IO_LIMIT = <PCI_IO_LIMIT: 29>
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libpci._macros.PCI_CB_BRIDGE_CTL_PARITY = <PCI_CB_BRIDGE_CTL_PARITY: 1>
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libpci._macros.PCI_CAP_ID_MSIX = <PCI_CAP_ID_MSIX: 17>
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libpci._macros.PCI_HT_SW_PC_RCR = <PCI_HT_SW_PC_RCR: 2>
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libpci._macros.PCI_EXP_FLAGS = <PCI_EXP_FLAGS: 2>
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libpci._macros.PCI_CLASS_SERIAL_SSA = <PCI_CLASS_SERIAL_SSA: 3074>
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libpci._macros.PCI_IOV_TOTALVF = <PCI_IOV_TOTALVF: 14>
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libpci._macros.PCI_RCLINK_LINK1 = <PCI_RCLINK_LINK1: 16>
    Constant from pci.h

libpci._macros.PCI_SSVID_VENDOR = <PCI_SSVID_VENDOR: 4>
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libpci._macros.PCI_PCIX_STATUS_BUS = <PCI_PCIX_STATUS_BUS: 65280>
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libpci._macros.PCI_CB_IO_LIMIT_1 = <PCI_CB_IO_LIMIT_1: 56>
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libpci._macros.PCI_CLASS_BRIDGE_ISA = <PCI_CLASS_BRIDGE_ISA: 1537>
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libpci._macros.PCI_HT_LCNF_LWO = <PCI_HT_LCNF_LWO: 28672>
    Constant from pci.h

libpci._macros.PCI_CLASS_COMMUNICATION_MODEM = <PCI_CLASS_COMMUNICATION_MODEM: 1795>
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libpci._macros.PCI_HT_FTR_ECTLTL = <PCI_HT_FTR_ECTLTL: 8>
    Constant from pci.h

libpci._macros.PCI_EXP_TYPE_ROOT_EC = <PCI_EXP_TYPE_ROOT_EC: 10>
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libpci._macros.PCI_AGP_STATUS_AGP3 = <PCI_AGP_STATUS_AGP3: 8>
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libpci._macros.PCI_HT_FTR_ISOCFC = <PCI_HT_FTR_ISOCFC: 1>
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libpci._macros.PCI_BASE_CLASS_INPUT = <PCI_BASE_CLASS_INPUT: 9>
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    Constant from pci.h

libpci._macros.PCI_VENDOR_ID_INTEL = <PCI_VENDOR_ID_INTEL: 32902>
    Constant from pci.h

libpci._macros.PCI_ERR_UNC_TRAIN = <PCI_ERR_UNC_TRAIN: 1>
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libpci._macros.PCI_EXP_SLTCTL_PWRF = <PCI_EXP_SLTCTL_PWRF: 2>
    Constant from pci.h

libpci._macros.PCI_EXP_SLTSTA_INTERLOCK = <PCI_EXP_SLTSTA_INTERLOCK: 128>
    Constant from pci.h

libpci._macros.PCI_AGP_COMMAND_GART64 = <PCI_AGP_COMMAND_GART64: 128>
    Constant from pci.h
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libpci._macros.PCI_HT_IDC_IDR_MASK = <PCI_HT_IDC_IDR_MASK: 268435457>
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libpci._macros.PCI_CLASS_BRIDGE_PCI = <PCI_CLASS_BRIDGE_PCI: 1540>
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libpci._macros.PCI_CB_IO_LIMIT_0 = <PCI_CB_IO_LIMIT_0: 48>
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libpci._macros.PCI_CLASS_REVISION = <PCI_CLASS_REVISION: 8>
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libpci._macros.PCI_CLASS_STORAGE_OTHER = <PCI_CLASS_STORAGE_OTHER: 384>
    Constant from pci.h

libpci._macros.PCI_HT_PRI_CMD_DUL = <PCI_HT_PRI_CMD_DUL: 4096>
    Constant from pci.h

libpci._macros.PCI_CAP_ID_VNDR = <PCI_CAP_ID_VNDR: 9>
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libpci._macros.PCI_EXP_RTCTL_SECEE = <PCI_EXP_RTCTL_SECEE: 1>
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libpci._macros.PCI_ACS_CTRL_CMPLT_RED = <PCI_ACS_CTRL_CMPLT_RED: 8>
    Constant from pci.h

libpci._macros.PCI_EXP_RTCTL_PMEIE = <PCI_EXP_RTCTL_PMEIE: 8>
    Constant from pci.h

libpci._macros.PCI_HT_SEC_CMD_DN = <PCI_HT_SEC_CMD_DN: 118>
    Constant from pci.h

libpci._macros.PCI_EXP_DEVCTL_NFERE = <PCI_EXP_DEVCTL_NFERE: 2>
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libpci._macros.PCI_EXP_DEVCTL_EXT_TAG = <PCI_EXP_DEVCTL_EXT_TAG: 256>
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libpci._macros.PCI_ROM_ADDRESS = <PCI_ROM_ADDRESS: 48>
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libpci._macros.PCI_BRIDGE_CONTROL = <PCI_BRIDGE_CONTROL: 62>
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libpci._macros.PCI_MSIX_BIR = <PCI_MSIX_BIR: 7>
    Constant from pci.h

libpci._macros.PCI_EXP_SLTCAP_PWR_SCL = <PCI_EXP_SLTCAP_PWR_SCL: 98304>
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libpci._macros.PCI_STATUS_SIG_TARGET_ABORT = <PCI_STATUS_SIG_TARGET_ABORT: 2048>
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libpci._macros.PCI_BASE_ADDRESS_SPACE = <PCI_BASE_ADDRESS_SPACE: 1>
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libpci._macros.PCI_EXP_RTCAP = <PCI_EXP_RTCAP: 30>
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libpci._macros.PCI_CLASS_PROCESSOR_ALPHA = <PCI_CLASS_PROCESSOR_ALPHA: 2832>
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libpci._macros.PCI_BRIDGE_CTL_FAST_BACK = <PCI_BRIDGE_CTL_FAST_BACK: 128>
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libpci._macros.PCI_STATUS = <PCI_STATUS: 6>
Constant from pci.h

libpci._macros.PCI_HT_LCTR_CRCERR = <PCI_HT_LCTR_CRCERR: 3840>
Constant from pci.h

libpci._macros.PCI_CLASS_BRIDGE_PCI_SEMI = <PCI_CLASS_BRIDGE_PCI_SEMI: 1545>
Constant from pci.h

libpci._macros.PCI_BASE_ADDRESS_SPACE_MEMORY = <PCI_BASE_ADDRESS_SPACE_MEMORY: 0>
Constant from pci.h

libpci._macros.PCI_CB_IO_LIMIT_1_HI = <PCI_CB_IO_LIMIT_1_HI: 58>
Constant from pci.h

libpci._macros.PCI_HT_UIDC_CE = <PCI_HT_UIDC_CE: 8>
Constant from pci.h

libpci._macros.PCI_EXP_LNKCTL_ASPM = <PCI_EXP_LNKCTL_ASPM: 3>
Constant from pci.h

libpci._macros.PCI_HT_PRI_LFCAP0 = <PCI_HT_PRI_LFCAP0: 14>
Constant from pci.h

libpci._macros.PCI_HT_IDC_IDR_II_32 = <PCI_HT_IDC_IDR_II_32: 16777215>
Constant from pci.h

libpci._macros.PCI_HT_DR_IDX_LIMIT_LO = <PCI_HT_DR_IDX_LIMIT_LO: 2>
Constant from pci.h

libpci._macros.PCI_HT_AM40_DMA_SLIM0 = <PCI_HT_AM40_DMA_SLIM0: 16>
Constant from pci.h

libpci._macros.PCI_AGP_STATUS_RATE1 = <PCI_AGP_STATUS_RATE1: 1>
Constant from pci.h

libpci._macros.PCI_PM_SIZEOF = <PCI_PM_SIZEOF: 8>
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libpci._macros.PCI_CAP_LIST_ID = <PCI_CAP_LIST_ID: 0>
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libpci._macros.PCI_HT_AM64_IDX_DMASL0 = <PCI_HT_AM64_IDX_DMASL0: 6>
Constant from pci.h

libpci._macros.PCI_HT_SEC_CMD_DUL = <PCI_HT_SEC_CMD_DUL: 4096>
Constant from pci.h

libpci._macros.PCI_BASE_CLASS_MULTIMEDIA = <PCI_BASE_CLASS_MULTIMEDIA: 4>
Constant from pci.h

libpci._macros.PCI_CB_LEGACY_MODE_BASE = <PCI_CB_LEGACY_MODE_BASE: 68>
Constant from pci.h

libpci._macros.PCI_HT_LFCAP_VEND = <PCI_HT_LFCAP_VEND: 32768>
Constant from pci.h

libpci._macros.PCI_PM_CTRL_DATA_SEL_MASK = <PCI_PM_CTRL_DATA_SEL_MASK: 7680>
Constant from pci.h

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libpci._macros.PCI_HT_VCS_SSUP_3 = <PCI_HT_VCS_SSUP_3: 1>
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libpci._macros.PCI_HT_VCS_SIZEOF = <PCI_HT_VCS_SIZEOF: 16>
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libpci._macros.PCI_ERR_CAP_ECRC_GENC = <PCI_ERR_CAP_ECRC_GENC: 32>
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libpci._macros.PCI_RCLINK_ESD = <PCI_RCLINK_ESD: 4>
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libpci._macros.PCI_STATUS_DEVSEL_MASK = <PCI_STATUS_DEVSEL_MASK: 1536>
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libpci._macros.PCI_HT_AM64_IDX_DMASB0 = <PCI_HT_AM64_IDX_DMASB0: 5>
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libpci._macros.PCI_HT_AM40_SIZEOF = <PCI_HT_AM40_SIZEOF: 12>
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libpci._macros.PCI_HT_CMD_TYP_HI_PRI = <PCI_HT_CMD_TYP_HI_PRI: 0>
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libpci._macros.PCI_EXP_RTSTA = <PCI_EXP_RTSTA: 32>
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libpci._macros.PCI_CLASS_OTHERS = <PCI_CLASS_OTHERS: 255>
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libpci._macros.PCI_EXT_CAP_ID_AER = <PCI_EXT_CAP_ID_AER: 1>
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libpci._macros.PCI_PCIX_STATUS_DESIGNED_MAX_MEM_READ_BYTE_COUNT = <PCI_PCIX_STATUS_DESIGNED_MAX_MEM_READ_BYTE_COUNT: 1>
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libpci._macros.PCI_PM_CTRL_DATA_SCALE_MASK = <PCI_PM_CTRL_DATA_SCALE_MASK: 24576>
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libpci._macros.PCI_MSIX_TABSIZE = <PCI_MSIX_TABSIZE: 2047>
    Constant from pci.h
libpci._macros.PCI_HT_UIDC_SIZEOF = <PCI_HT_UIDC_SIZEOF: 12>
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libpci._macros.PCI_ERR_UNC_ACS_VIOL = <PCI_ERR_UNC_ACS_VIOL: 2097152>
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libpci._macros.PCI_HT_SEC_MLU = <PCI_HT_SEC_MLU: 21>
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libpci._macros.PCI_CAPABILITY_LIST = <PCI_CAPABILITY_LIST: 52>
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libpci._macros.PCI_MSI_FLAGS_MASK_BIT = <PCI_MSI_FLAGS_MASK_BIT: 256>
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libpci._macros.PCI_PWR_CAP = <PCI_PWR_CAP: 12>
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libpci._macros.PCI_EXP_SLTSTA_ATNB = <PCI_EXP_SLTSTA_ATNB: 1>
    Constant from pci.h

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`libpci._macros.PCI_PCIX_BRIDGE_SEC_STATUS_133MHZ = <PCI_PCIX_BRIDGE_SEC_STATUS_133MHZ: 2>`
Constant from pci.h

`libpci._macros.PCI_CLASS_INPUT_SCANNER = <PCI_CLASS_INPUT_SCANNER: 2307>`
Constant from pci.h

`libpci._macros.PCI_HT_SW_SWINF_HP = <PCI_HT_SW_SWINF_HP: 262144>`
Constant from pci.h

`libpci._macros.PCI_HT_LFRER_1400 = <PCI_HT_LFRER_1400: 8>`
Constant from pci.h

`libpci._macros.PCI_HT_CMD_TYP_AM = <PCI_HT_CMD_TYP_AM: 40960>`
Constant from pci.h

`libpci._macros.PCI_HT_CMD_TYP_RID = <PCI_HT_CMD_TYP_RID: 34816>`
Constant from pci.h

`libpci._macros.PCI_EXT_CAP_ID_VC2 = <PCI_EXT_CAP_ID_VC2: 9>`
Constant from pci.h

`libpci._macros.PCI_ERR_ROOT_COMMAND = <PCI_ERR_ROOT_COMMAND: 44>`
Constant from pci.h

`libpci._macros.PCI_EXP_LNKSTA_BWMGMT = <PCI_EXP_LNKSTA_BWMGMT: 16384>`
Constant from pci.h

`libpci._macros.PCI_HT_PRI_CMD_BUID = <PCI_HT_PRI_CMD_BUID: 31>`
Constant from pci.h

`libpci._macros.PCI_EXP_SLTCTL_MRLS = <PCI_EXP_SLTCTL_MRLS: 4>`
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`libpci._macros.PCI_EXP_SLTCTL_PWRC = <PCI_EXP_SLTCTL_PWRC: 1024>`
Constant from pci.h

`libpci._macros.PCI_EXP_LNKCAP_WIDTH = <PCI_EXP_LNKCAP_WIDTH: 1008>`
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`libpci._macros.PCI_IOV_CAP = <PCI_IOV_CAP: 4>`
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`libpci._macros.PCI_EXP_DEVCAP_EXT_TAG = <PCI_EXP_DEVCAP_EXT_TAG: 32>`
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`libpci._macros.PCI_VNDR_LENGTH = <PCI_VNDR_LENGTH: 2>`
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`libpci._macros.PCI_HT_CMD = <PCI_HT_CMD: 2>`
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`libpci._macros.PCI_EXT_CAP_ID_RCILINK = <PCI_EXT_CAP_ID_RCILINK: 6>`
Constant from pci.h

`libpci._macros.PCI_HT_FTR_CRCTM = <PCI_HT_FTR_CRCTM: 4>`
Constant from pci.h

`libpci._macros.PCI_AGP_COMMAND = <PCI_AGP_COMMAND: 8>`
Constant from pci.h

`libpci._macros.PCI_HT_AM40_SBW_BASE = <PCI_HT_AM40_SBW_BASE: 1048575>`
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libpci._macros.PCI_BASE_CLASS_PROCESSOR = <PCI_BASE_CLASS_PROCESSOR: 11>
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libpci._macros.PCI_EXP_DEVSTA_TRPND = <PCI_EXP_DEVSTA_TRPND: 32>
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libpci._macros.PCI_HT_DR_BL_LO = <PCI_HT_DR_BL_LO: 4294967040>
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libpci._macros.PCI_HT_SW_BLR_LIM0_HI = <PCI_HT_SW_BLR_LIM0_HI: 3>
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libpci._macros.PCI_EXP_SLTCTL_ATNB = <PCI_EXP_SLTCTL_ATNB: 1>
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libpci._macros.PCI_HT_SW_SIZEOF = <PCI_HT_SW_SIZEOF: 24>
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libpci._macros.PCI_HT_PRI_CMD = <PCI_HT_PRI_CMD: 2>
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libpci._macros.PCI_EXP_DEVCAP_PAYLOAD = <PCI_EXP_DEVCAP_PAYLOAD: 7>
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libpci._macros.PCI_HT_MSIM_ADDR_LO = <PCI_HT_MSIM_ADDR_LO: 4>
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libpci._macros.PCI_HT_LCTR_ISOCEN = <PCI_HT_LCTR_ISOCEN: 4096>
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libpci._macros.PCI_EXP_LNKCTL2 = <PCI_EXP_LNKCTL2: 48>
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libpci._macros.PCI_CB_BRIDGE_CTL_MASTER_ABORT = <PCI_CB_BRIDGE_CTL_MASTER_ABORT: 32>
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libpci._macros.PCI_EXP_SLTCAP2 = <PCI_EXP_SLTCAP2: 52>
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libpci._macros.PCI_TPH_CAPABILITIES = <PCI_TPH_CAPABILITIES: 4>
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libpci._macros.PCI_PCIX_COMMAND_MAX_OUTSTANDING_SPLIT_TRANS = <PCI_PCIX_COMMAND_MAX_OUTSTANDING_SPLIT_TRANS: 1>
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libpci._macros.PCI_AGP_COMMAND_FW = <PCI_AGP_COMMAND_FW: 16>
Constant from pci.h

libpci._macros.PCI_PCIX_BRIDGE_STR_COMMITMENT_LIMIT = <PCI_PCIX_BRIDGE_STR_COMMITMENT_LIM
Constant from pci.h

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libpci._macros.PCI_BASE_ADDRESS_4 = <PCI_BASE_ADDRESS_4: 32>
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libpci._macros.PCI_MEMORY_LIMIT = <PCI_MEMORY_LIMIT: 34>
Constant from pci.h

libpci._macros.PCI_HT_SEC_CMD_HH = <PCI_HT_SEC_CMD_HH: 256>
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libpci._macros.PCI_HT_ECSA_ADDR_BUS = <PCI_HT_ECSA_ADDR_BUS: 267386880>
Constant from pci.h

libpci._macros.PCI_IOV_OFFSET = <PCI_IOV_OFFSET: 20>
Constant from pci.h

libpci._macros.PCI_BASE_ADDRESS_MEM_TYPE_1M = <PCI_BASE_ADDRESS_MEM_TYPE_1M: 2>
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libpci._macros.PCI_HT_SW_PC_NPCR = <PCI_HT_SW_PC_NPCR: 1>
Constant from pci.h

libpci._macros.PCI_BRIDGE_CTL_DISCARD_TIMER_SERR_EN = <PCI_BRIDGE_CTL_DISCARD_TIMER_SERR_E
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libpci._macros.PCI_CB_BRIDGE_CTL_ISA = <PCI_CB_BRIDGE_CTL_ISA: 4>
Constant from pci.h

libpci._macros.PCI_EXP_SLTCTL_PRSD = <PCI_EXP_SLTCTL_PRSD: 8>
Constant from pci.h

libpci._macros.PCI_EXP_LNKCTL2_CMPLNC_SOS = <PCI_EXP_LNKCTL2_CMPLNC_SOS: 2048>
Constant from pci.h

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libpci._macros.PCI_ACS_CAP_FORWARD = <PCI_ACS_CAP_FORWARD: 16>
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libpci._macros.PCI_CB_SUBSYSTEM_VENDOR_ID = <PCI_CB_SUBSYSTEM_VENDOR_ID: 64>
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libpci._macros.PCI_LTR_MAX_SNOOP = <PCI_LTR_MAX_SNOOP: 4>
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libpci._macros.PCI_BASE_CLASS_SATELLITE = <PCI_BASE_CLASS_SATELLITE: 15>
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libpci._macros.PCI_EXP_DEVCTL_FLRESET = <PCI_EXP_DEVCTL_FLRESET: 32768>
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libpci._macros.PCI_EXT_CAP_ID_MFVC = <PCI_EXT_CAP_ID_MFVC: 8>
    Constant from pci.h

libpci._macros.PCI_CAP_ID_SATA = <PCI_CAP_ID_SATA: 18>
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libpci._macros.PCI_EXP_LNKCTL_RCB = <PCI_EXP_LNKCTL_RCB: 8>
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libpci._macros.PCI_VPD_ADDR = <PCI_VPD_ADDR: 2>
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libpci._macros.PCI_HT_EH_RE = <PCI_HT_EH_RE: 512>
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libpci._macros.PCI_PCIX_BRIDGE_SEC_STATUS_SC_OVERRUN = <PCI_PCIX_BRIDGE_SEC_STATUS_SC_OVERRUN: 1>
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libpci._macros.PCI_EXP_LNKSTA_WIDTH = <PCI_EXP_LNKSTA_WIDTH: 1008>
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libpci._macros.PCI_HT_SW_CMD_VIBFL = <PCI_HT_SW_CMD_VIBFL: 256>
    Constant from pci.h

libpci._macros.PCI_HT_LFRER_1000 = <PCI_HT_LFRER_1000: 6>
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libpci._macros.PCI_VPD_DATA = <PCI_VPD_DATA: 4>
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libpci._macros.PCI_HT_SW_CMD_VIBERR = <PCI_HT_SW_CMD_VIBERR: 128>
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libpci._macros.PCI_HT_RM_CTR_RETEN = <PCI_HT_RM_CTR_RETEN: 16>
    Constant from pci.h

libpci._macros.PCI_MSI_FLAGS_QMASK = <PCI_MSI_FLAGS_QMASK: 14>
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libpci._macros.PCI_EXP_LNKSTA_SL_CLK = <PCI_EXP_LNKSTA_SL_CLK: 4096>
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libpci._macros.PCI_HT_SW_BLRD = <PCI_HT_SW_BLRD: 16>
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libpci._macros.PCI_ATS_CTRL_ENABLE = <PCI_ATS_CTRL_ENABLE: 32768>
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libpci._macros.PCI_EXP_SLTCTL_PWRI = <PCI_EXP_SLTCTL_PWRI: 768>
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libpci._macros.PCI_EXP_LNKCTL = <PCI_EXP_LNKCTL: 16>
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    Constant from pci.h

libpci._macros.PCI_HT_SW_PC_PDWT = <PCI_HT_SW_PC_PDWT: 9>
    Constant from pci.h

libpci._macros.PCI_ROM_ADDRESS1 = <PCI_ROM_ADDRESS1: 56>
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libpci._macros.PCI_HT_CMD_TYP_VCS = <PCI_HT_CMD_TYP_VCS: 47104>
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libpci._macros.PCI_HT_ECSA_ADDR = <PCI_HT_ECSA_ADDR: 4>
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libpci._macros.PCI_EXP_LNKCAP_L1 = <PCI_EXP_LNKCAP_L1: 229376>
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libpci._macros.PCI_EXP_DEVCAP = <PCI_EXP_DEVCAP: 4>
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libpci._macros.PCI_CLASS_INPUT_GAMEPORT = <PCI_CLASS_INPUT_GAMEPORT: 2308>
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libpci._macros.PCI_HT_EH_ONFE = <PCI_HT_EH_ONFE: 2048>
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libpci._macros.PCI_AGP_STATUS_FW = <PCI_AGP_STATUS_FW: 16>
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libpci._macros.PCI_HT_DR_IDX_BASE_HI = <PCI_HT_DR_IDX_BASE_HI: 1>
    Constant from pci.h

libpci._macros.PCI_SID_CHASSIS_NR = <PCI_SID_CHASSIS_NR: 3>
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libpci._macros.PCI_AGP_VERSION = <PCI_AGP_VERSION: 2>
    Constant from pci.h

libpci._macros.PCI_ERR_UNC_UNX_COMP = <PCI_ERR_UNC_UNX_COMP: 65536>
    Constant from pci.h

libpci._macros.PCI_PCIX_SIZEOF = <PCI_PCIX_SIZEOF: 4>
    Constant from pci.h

libpci._macros.PCI_CB_MEMORY_LIMIT_0 = <PCI_CB_MEMORY_LIMIT_0: 32>
    Constant from pci.h

libpci._macros.PCI_CB_IO_BASE_1 = <PCI_CB_IO_BASE_1: 52>
    Constant from pci.h

libpci._macros.PCI_ACS_EGRESS_CTRL = <PCI_ACS_EGRESS_CTRL: 8>
    Constant from pci.h
```

3.2.2 C Types

Type definitions and enumerations found in pci.h.

```
class libpci._types.id_bucket
    struct id_bucket (private).
```

```
class libpci._types.id_entry
    struct id_entry (private).
```

```
class libpci._types.pci_access
    struct pci_access from pci.h.
```

```
    buscentric
        Structure/Union member
```

```
    cached_dev
        Structure/Union member
```

```
    current_id_bucket
        Structure/Union member
```

```
    debug
        Structure/Union member
```

```
    debugging
        Structure/Union member
```

```
    devices
        Structure/Union member
```

```
    error
        Structure/Union member
```

```
    fd
        Structure/Union member
```

```
    fd_pos
        Structure/Union member
```

```
    fd_rw
        Structure/Union member
```

```
    fd_vpd
        Structure/Union member
```

```
    free_id_name
        Structure/Union member
```

```
    id_cache_status
        Structure/Union member
```

```
    id_file_name
        Structure/Union member
```

```
    id_hash
        Structure/Union member
```

```
    id_load_failed
        Structure/Union member
```

```
    id_lookup_mode
        Structure/Union member
```

```
    method
        Structure/Union member
```

```
    methods
        Structure/Union member
```

numeric_ids
Structure/Union member

params
Structure/Union member

warning
Structure/Union member

writable
Structure/Union member

class `libpci._types.pci_access_type`
enum `pci_access_type` from `pci.h`.

class `libpci._types.pci_cap`
struct `pci_cap` from `pci.h`.

addr
Structure/Union member

id
Structure/Union member

next
Structure/Union member

type
Structure/Union member

class `libpci._types.pci_dev`
struct `pci_dev` from `pci.h`.

access
Structure/Union member

aux
Structure/Union member

base_addr
Structure/Union member

bus
Structure/Union member

cache
Structure/Union member

cache_len
Structure/Union member

dev
Structure/Union member

device_class
Structure/Union member

device_id
Structure/Union member

domain
Structure/Union member

first_cap
Structure/Union member

func
Structure/Union member

hdrtype
Structure/Union member

irq
Structure/Union member

known_fields
Structure/Union member

methods
Structure/Union member

module_alias
Structure/Union member

next
Structure/Union member

phy_slot
Structure/Union member

rom_base_addr
Structure/Union member

rom_file
Structure/Union member

size
Structure/Union member

vendor_id
Structure/Union member

class libpci._types.**pci_filter**
struct pci_filter from pci.h.

bus
Structure/Union member

device
Structure/Union member

domain
Structure/Union member

func
Structure/Union member

slot
Structure/Union member

vendor
Structure/Union member

class libpci._types.**pci_lookup_mode**
enum pci_lookup_mode from pci.h.

```
class libpci._types.pci_methods
    struct pci_methods (private).
```

```
class libpci._types.pci_param
    struct pci_param from pci.h.
```

```
    help
        Structure/Union member
```

```
    param
        Structure/Union member
```

```
    pci_param
        Structure/Union member
```

```
    value
        Structure/Union member
```

```
    value_mallocated
        Structure/Union member
```

3.2.3 C Functions

Low-level interface to libpci (using ctypes).

```
class libpci._functions.pci_access
    struct pci_access from pci.h.
```

```
    _fields_ = [('method', <class 'ctypes.c_uint'>), ('writable', <class 'ctypes.c_int'>), ('buscentric', <class 'ctypes.c_int'>)]
```

```
    buscentric
        Structure/Union member
```

```
    cached_dev
        Structure/Union member
```

```
    current_id_bucket
        Structure/Union member
```

```
    debug
        Structure/Union member
```

```
    debugging
        Structure/Union member
```

```
    devices
        Structure/Union member
```

```
    error
        Structure/Union member
```

```
    fd
        Structure/Union member
```

```
    fd_pos
        Structure/Union member
```

```
    fd_rw
        Structure/Union member
```

```
    fd_vpd
        Structure/Union member
```

free_id_name
Structure/Union member

id_cache_status
Structure/Union member

id_file_name
Structure/Union member

id_hash
Structure/Union member

id_load_failed
Structure/Union member

id_lookup_mode
Structure/Union member

method
Structure/Union member

methods
Structure/Union member

numeric_ids
Structure/Union member

params
Structure/Union member

warning
Structure/Union member

writable
Structure/Union member

Contributing

Contributions are welcome, and they are greatly appreciated! Every little bit helps, and credit will always be given. You can contribute in many ways:

4.1 Types of Contributions

4.1.1 Report Bugs

Report bugs at <https://github.com/zyga/libpci/issues>.

If you are reporting a bug, please include:

- Your operating system name and version.
- Any details about your local setup that might be helpful in troubleshooting.
- Detailed steps to reproduce the bug.

4.1.2 Fix Bugs

Look through the GitHub issues for bugs. Anything tagged with “bug” is open to whoever wants to implement it.

4.1.3 Implement Features

Look through the GitHub issues for features. Anything tagged with “feature” is open to whoever wants to implement it.

4.1.4 Write Documentation

libpci could always use more documentation, whether as part of the official libpci docs, in docstrings, or even on the web in blog posts, articles, and such.

4.1.5 Submit Feedback

The best way to send feedback is to file an issue at <https://github.com/zyga/libpci/issues>.

If you are proposing a feature:

- Explain in detail how it would work.
- Keep the scope as narrow as possible, to make it easier to implement.
- Remember that this is a volunteer-driven project, and that contributions are welcome :)

4.2 Get Started!

Ready to contribute? Here's how to set up *libpci* for local development.

1. Fork the *libpci* repo on GitHub.

2. Clone your fork locally:

```
$ git clone git@github.com:your_name_here/libpci.git
```

3. Install your local copy into a virtualenv. Assuming you have virtualenvwrapper installed, this is how you set up your fork for local development:

```
$ mkvirtualenv libpci
$ cd libpci/
$ python3 setup.py develop
```

4. Create a branch for local development:

```
$ git checkout -b name-of-your-bugfix-or-feature
```

Now you can make your changes locally.

5. When you're done making changes, check that your changes pass flake8 and the tests, including testing other Python versions with tox:

```
$ flake8 libpci
$ python3 setup.py test
$ tox
```

To get flake8 and tox, just pip install them into your virtualenv.

6. Commit your changes and push your branch to GitHub:

```
$ git add .
$ git commit -m "Your detailed description of your changes."
$ git push origin name-of-your-bugfix-or-feature
```

7. Submit a pull request through the GitHub website.

4.3 Pull Request Guidelines

Before you submit a pull request, check that it meets these guidelines:

1. The pull request should include tests.
2. If the pull request adds functionality, the docs should be updated. Put your new functionality into a function with a docstring, and add the feature to the list in README.rst.
3. The pull request should work for Python 3.4. Check https://travis-ci.org/zyga/libpci/pull_requests and make sure that the tests pass for all supported Python versions.

4.4 Tips

To run a subset of tests:

```
$ python3 -m unittest libpci.tests
```

Credits

5.1 Development Lead

- Zygmunt Krynicki <zygmunt.krynicki@canonical.com>

5.2 Contributors

None yet. Why not be the first?

History

6.1 0.2 (2015-04-24)

- Use sub-commands for easier extensibility and less ambiguity.
- Move all flag control options to the ‘pci-lookup’ command.
- Move the PCI device and vendor name look up to their own sub-commands.
- Add LibPCI.lookup_subsystem_device_name().
- Add ‘subsystem-device’ sub-command.

6.2 0.1.1 (2015-04-23)

- Fix architecture detection bug that prevented libpci from working on Fedora.

6.3 0.1 (2015-04-23)

- First release on PyPI.

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